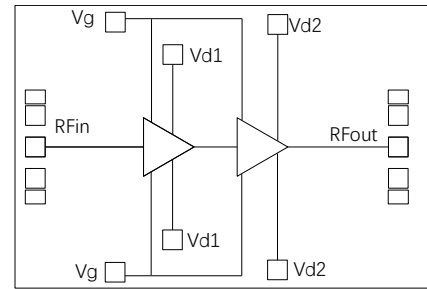


GaN MMIC Power Amplifier Chip, 4.0-8.0 GHz

Performance characteristics

- Frequency range: 4.0~8.0GHz
- Psat: 43dBm
- Power gain: 13dB
- Power supply: 28V/420mA
- 50ohm input/output
- Chip size:
3.3mm×2.5mm×0.1mm

Block Diagram



Product Introduction

GPA4-8-43 is a power amplifier chip manufactured using GaN HEMT technology. The working frequency band covers 4.0-8.0GHz, providing a power gain of 13dB at a supply voltage of 28V, with a saturated output power of 43dBm. The chip is grounded through the back through-hole. Mainly used in communication systems, high-power transceiver components, and other fields.

DC electrical parameters (T_A=+25°C)

Parameter	Min	Typ	Max	Unit
Gate bias voltage		-2.2		V
Drain working voltage		28		V
Quiescent drain current		420		mA
Dynamic drain current		2000		mA

Microwave electrical parameters (T_A=+25°C, V_d=+28V, pulse width 300us, duty cycle 30%)

Parameter	Min	Typ	Max	Unit
Frequency range		4.0~8.0		GHz
Psat		43		dBm
PAE		40		%
Power gain		13		dB
Power gain flatness		±0.5		dB
Input/output return loss		-14		dB

Absolute maximum ratings^[1]

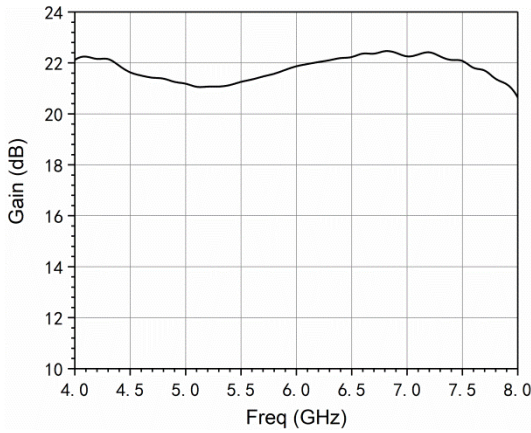
Parameter	Limit value
Drain voltage	+30V
Input power	+30dBm
Operating temperature	-55°C~+85°C
Storage temperature	-65°C~+120°C

[1] Exceeding any of these limits may cause permanent damage.

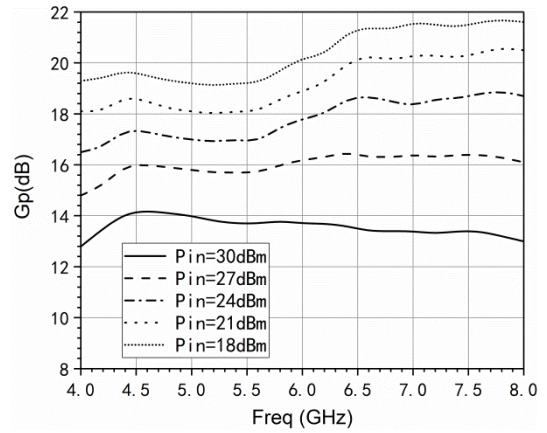
GaN MMIC Power Amplifier Chip, 4.0-8.0 GHz

Typical performance curves (V_d : +28V, quiescent $I_d=420mA$, pulse width 300us, duty cycle 30%)

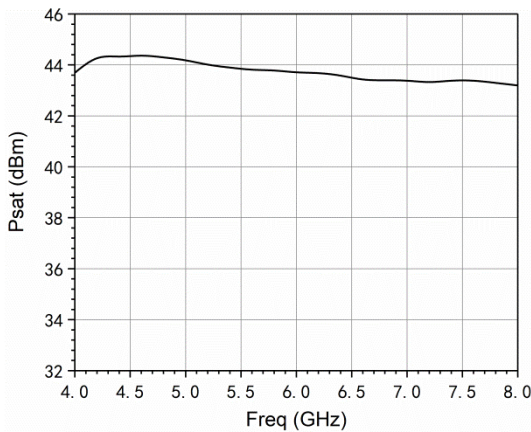
Gain vs. frequency (@ $P_{in}=-25dBm$)



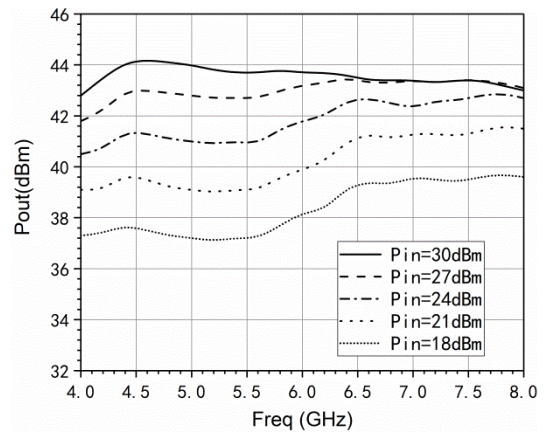
Power gain vs. frequency



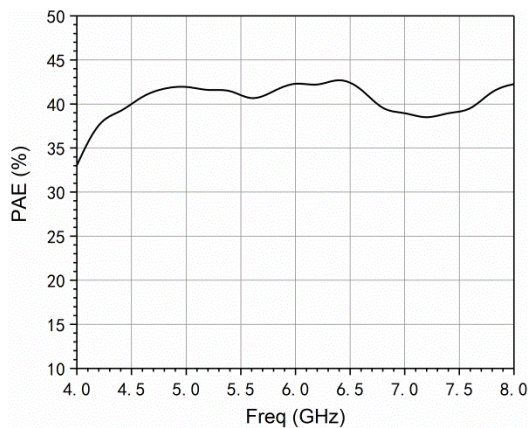
Saturated power vs. frequency



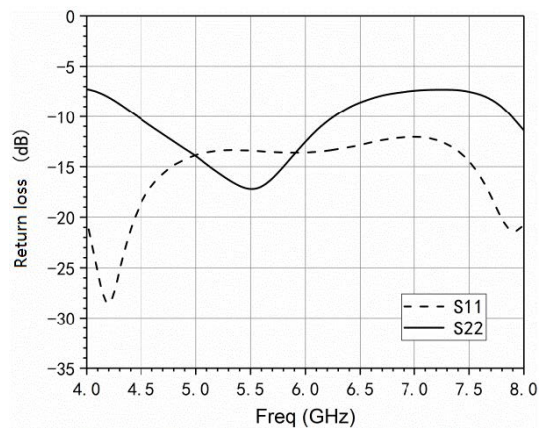
Output power vs. frequency



PAE vs. frequency (@ P_{sat})

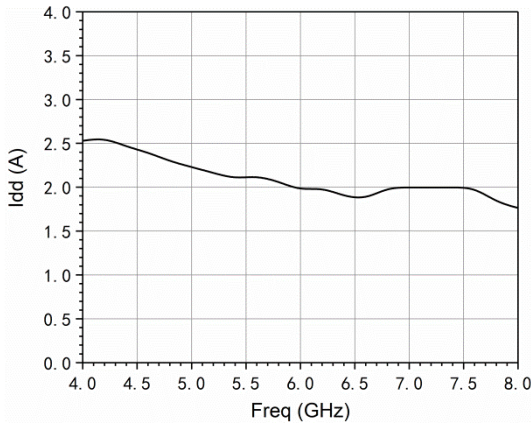


Return loss vs. frequency

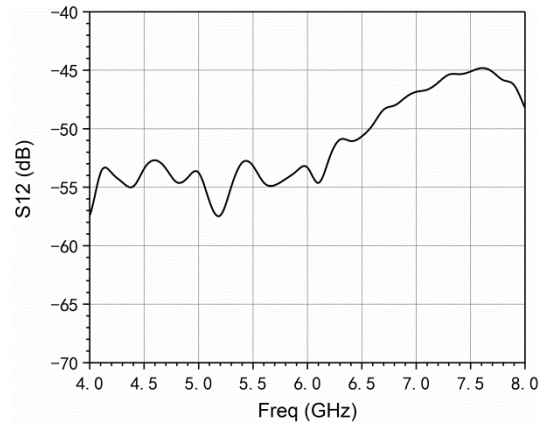


GaN MMIC Power Amplifier Chip, 4.0-8.0 GHz

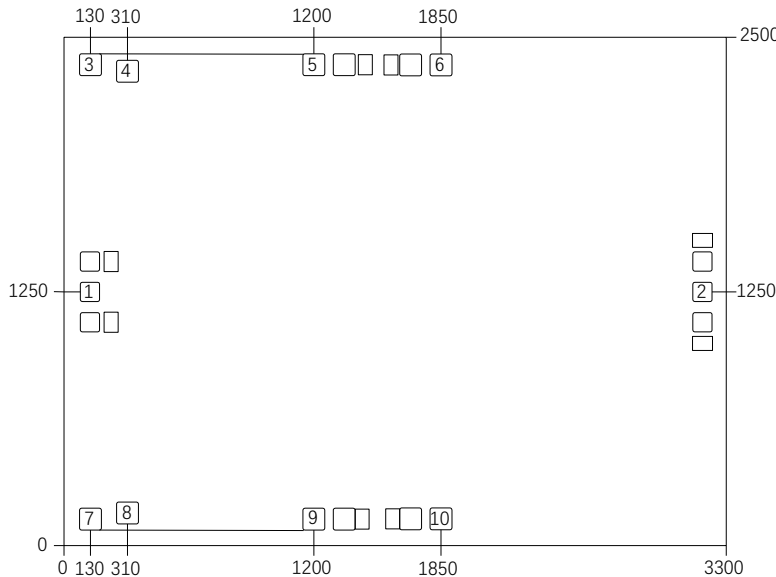
I_{dd} VS. frequency



Reverse isolation vs. frequency (@P_{sat})



Outline Dimensions



Notes:

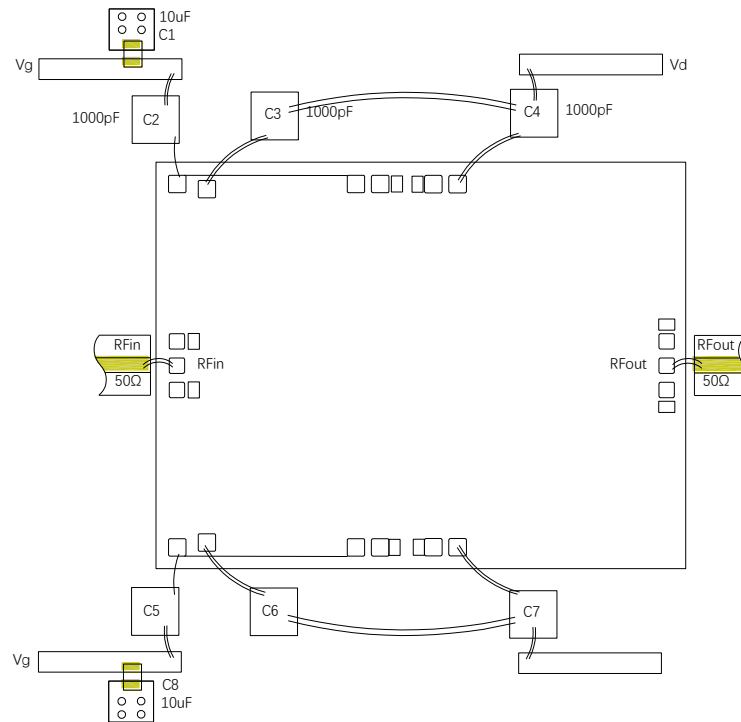
1. Unit: μm
2. Gold plating on bonding pads
3. Dimensional tolerance: $\pm 20 \mu\text{m}$

Pad Definition

Pad Number	Function	Description	Dimensions
1	IN	RF input, external 50 ohm system, no need for external blocking capacitor	100 × 100 μm
2	OUT	RF output, external 50 ohm system, no need for external blocking capacitor	100 × 100 μm
4、6、8、10	V _d	Drain power supply, 28V	100 × 100 μm
3、5、7、9	V _g	Gate power supply, -2.2V, quiescent current 400mA	100 × 100 μm

GaN MMIC Power Amplifier Chip, 4.0-8.0 GHz

Suggested assembly diagram



Note: C2, C3, C4, C5, C6, C7 are 1000pF single-layer ceramic capacitors. To ensure more stable performance of the amplifier, it is recommended to weld ceramic capacitors with the recommended capacitance values in the assembly diagram at the feeding end for filtering. The number of filtering capacitors can be increased or different capacitance values can be combined according to actual needs.

Note:

1. Please assemble and use in a purified environment, store in anti-static containers, and keep dry
2. The back of the chip is grounded with gold backing. Please ensure that the back is in full contact with the ground and well grounded during use
3. Use gold tin solder with a ratio of 80/20 to sinter, with a sintering temperature not exceeding 300 °C and a sintering time as short as possible, not exceeding 20 seconds
4. This product is an electrostatic sensitive device. Please pay attention to anti-static measures during storage and use
5. Do not attempt to clean the surface of the chip using dry or wet chemical methods
6. If you have any questions, please contact the supplier



This product is sensitive to static electricity. Please pay attention to anti-static measures during use