

Performance characteristics

Frequency Range: 2-20GHzInsertion loss: 0.9dB typ.

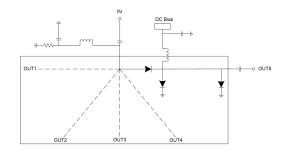
Isolation: 43 dB typ.P-1dB: 26dBm

500hm input / output100% on-wafer testing

• Chip size: 2.32 x 2.15 x 0.1mm

Silicon nitride passivation, scratch protection

Functional Block Diagram



Product Introduction

GSW510C/D are two GaAs PIN reflective single-pole five-throw switch chips, with 50Ω matching at the input/output ends, a frequency range of 2 to 20GHz, and -5V/+5V control. ISW510C/D are mirror versions of each other. Built-in bias network and DC blocking capacitors are easy to use. It has excellent switching characteristics and port standing wave characteristics throughout the entire operating frequency range, and is very suitable for microwave hybrid integrated circuits, multi-chip modules, and low-power systems. The switch chip uses on-chip through-hole metallization technology to ensure good grounding, and does not require additional grounding measures. It is simple and convenient to use. The back of the chip is metallized, which is suitable for eutectic sintering or conductive adhesive bonding processes.

Use restriction parameter ¹				
Maximum input voltage	2 5V			
Maximum input power (test frequency 17GHz, test	+33dBm CW			
time 30 minutes) Operating temperature	-55 ~ +85°C			
storage temperature	-65 ~ +150°C			

[1] Exceeding any of these maximum limits may cause permanent damage.

Electrical performance parameters (TA = +25°C)					
index	Minimum	Typical Value	Maximum	unit	
Frequency Range	2-20			GHz	
Insertion loss	-	0.9	1.4	dB	
Isolation	33	43	-	dB	
Input return loss	13	18	-	dB	
Output return loss	14	21	-	dB	
P-1dB @ 2GHz/10GHz/ 17GHz -		26/26/24	-	dBm	
Switching speed	-	30	-	ns	

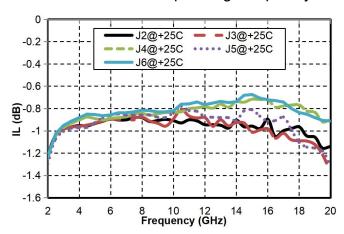
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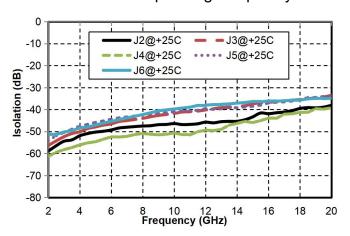


Main index test curve

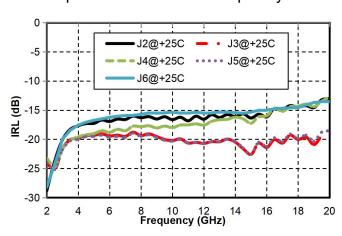




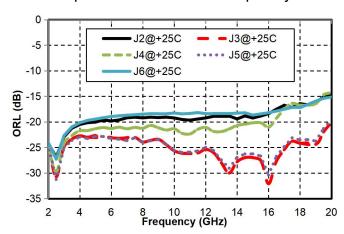
Isolation vs. Operating Frequency



Input Return Loss vs. Frequency



Output Return Loss vs. Frequency



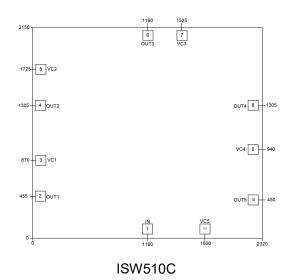
Typical Driver Connections

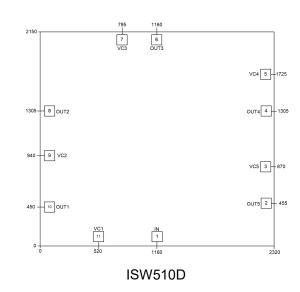
Control Port			Output conduction condition						
VC1	VC2	VC3	VC4	VC5	J2-J1	J3-J1	J4-J1	J5-J1	J6-J1
-10 mA	20 mA	20 mA	20 mA	20 mA	Continuity	isolation	isolation	isolation	isolation
20 mA	-10	20 mA	20 mA	20 mA	isolation	Continuity	isolation	isolation	isolation
	mA								
20 mA	20 mA	-10	20 mA	20 mA	isolation	isolation	Continuity	isolation	isolation
		mA							
20 mA	20 mA	20 mA	-10	20 mA	isolation	isolation	isolation	Continuity	isolation
			mA						
20 mA	20 mA	20 mA	20 mA	-10 mA	isolation	isolation	isolation	isolation	Continuity

Note: V = +1.35 V, I = +20 mA; V = -3.2 V, I = -10 mA (including J1 on-chip resistor RIN = 50 ohm voltage divider)



Appearance structure





All units in the figure are micrometers

Bonding point definition (ISW510C)					
Bonding point number	Function Symbol	Functional Description			
1	IN(J1)	RF input signal terminal			
2, 4, 6, 8, 10	OUT1(J2), OUT2(J3), OUT3(J4), OUT4(J5), OUT5(J6)	RF output signal terminal			
3, 5, 7, 9, 11	VC1, VC2, VC3, VC4, VC5	Signal control terminal			
		The bottom of the chip			
Chip bottom	GND	needs to be well			
		grounded to RF and DC			

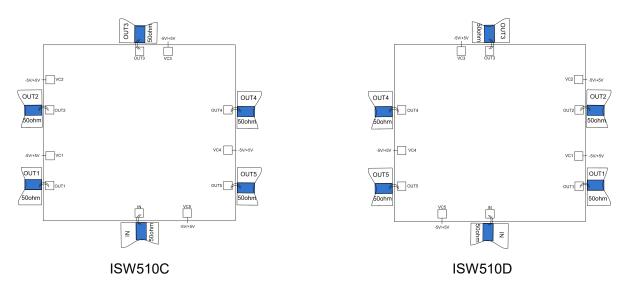
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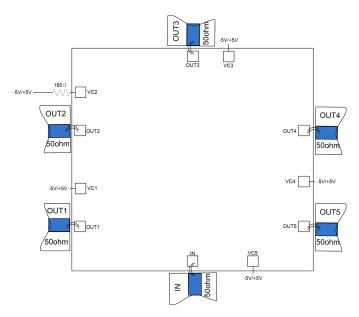
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Recommended assembly circuit diagram



Recommended use



+5V is connected in series with R=180 ohm resistor , V =+ 1.35V , I=+20mA . -5V is connected in series with R=180 ohm resistor , V=-3.2V, I=-10mA .

Note: If you need to reduce power consumption, you need to connect +-5V /-5V in series with resistors of different resistance values. For example: +5V in series with R=370 ohm resistor: V=+1.3V, I=+10mA; -5V in series with R=180 ohm resistor V=-3.2V, I=-10mA. (Including the internal resistor RIN=50 ohm voltage divider at J1 end). Please contact the manufacturer for specific usage.

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