

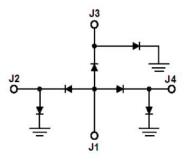
# GaAs PIN Reflective Single-pole Triple-throw Switch Chip, 0.05- 40GHz

Performance characteristics

- Frequency range: 0.05 40 GHz
- Insertion loss : 0.7dB typ.
- Isolation: 44 dB typ.
- P-1dB: 30dBm @17GHz
- 500hm input / output
- 100% on-wafer testing
- Chip size: 1.67 x 1.37 x 0.1mm
- Silicon nitride passivation, scratch protection

## **Product Introduction**





GSW3H is a GaAs PIN reflective single-pole triple-throw switch chip, input/output end  $50\Omega$  matching, frequency range covers  $0.05 \sim 40$ GHz, and adopts -5V/+5V control. It has excellent switching characteristics and port standing wave characteristics in the entire operating frequency range, and is very suitable for microwave hybrid integrated circuits, multi-chip modules and low-power systems. The switch chip uses on-chip through-hole metallization process to ensure good grounding, does not require additional grounding measures, and is simple and convenient to use. The back of the chip is metallized, which is suitable for eutectic sintering or conductive adhesive bonding process.

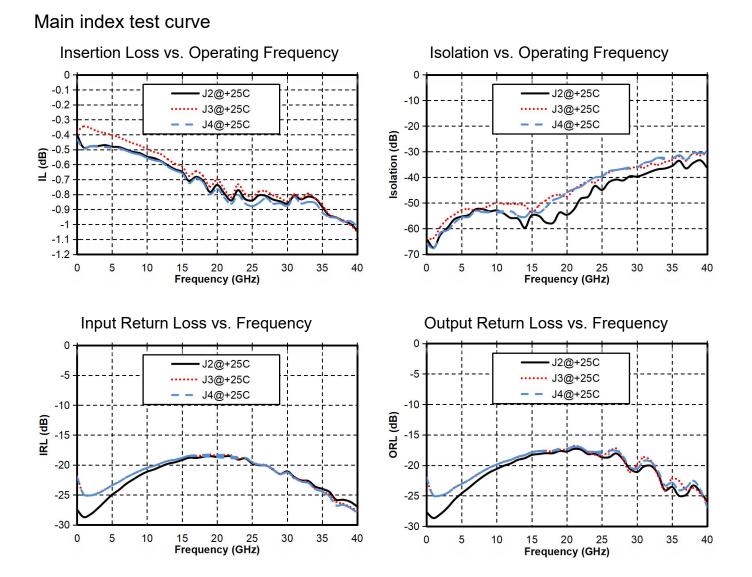
#### Use restriction parameter <sup>1</sup>

Maximum input voltage	2 5V
Maximum input power	+36dBm CW
Operating temperature	-55 ~ +85°C
storage temperature	-65 ~ +150°C

[1] Exceeding any of these maximum limits may cause permanent damage.

Electrical performance parameters ( TA = +25°C)						
index	Minimum	Typical Value	Maximum	unit		
Frequency Range	0.05-18			G Hz		
Insertion loss	-	0.6	0.7	dB		
Isolation	47	53	-	dB		
Input return loss	18	twenty two	-	dB		
Output return loss	18	twenty two	-	dB		
Frequency Range	18-40			G Hz		
Insertion loss	-	0.8	1.1	dB		
Isolation	30	37	-	dB		
Input return loss	18	twenty one	-	dB		
Output return loss	18	20	-	dB		
P-1dB @17GHz	-	28.5	-	dBm		
Switching speed	-	20	-	ns		

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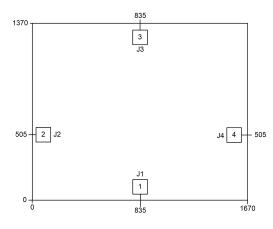
# **Typical Driver Connections**

CONTROL LEVEL (DC CURRENT)		RF OUTPUT STATE			
J2	J3	J4	J2-J1	J3-J1	J4-J1
-10mA	+ 20mA	+ 20mA	Low Loss	Isolation	Isolation
+ 20mA	-10mA	+ 20mA	Isolation	Low Loss	Isolation
+ 20mA	+ 20mA	-10mA	Isolation	Isolation	Low Loss
Note: V ≈ +1.35 V, I ≈ +2 0 mA; V ≈ - 3.20 V, I ≈ -10 mA (including J1 end RIN = 50 ohm					
resistor voltage divider ) , +5 V/-5 V series R ≈ 180 ohm resistor					



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### Appearance structure

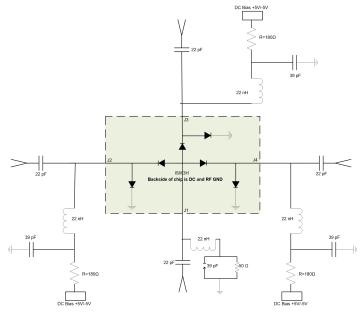


All units in the figure are micrometers

## Bonding point definition

Bonding point number	Function Symbol	Functional Description	
1 , 2, 3, 4	J1, J2, J3, J4	RF signal terminal	
Chip bottom	GND	The bottom of the chip needs to be well grounded	
	GND	to RF and DC	

### Recommended circuit diagram



If you need to reduce power consumption, you need to connect +5V/-5V in series with different resistance values. +5V series R  $\approx$  370 ohm resistor, V=+1.3V, I=+10mA; -5V series R  $\approx$  180 ohm resistor, V=-3.2V, I=-10mA . Users can change the resistance value according to their own situation. If you have any questions, please contact the manufacturer.