

## GaAs PIN reflective single-pole triple-throw switch chip, 2-20GHz

#### Performance characteristics

Frequency range: 2-20GHzInsertion loss: 0.8dB typ.

• Isolation: 48 dB typ.

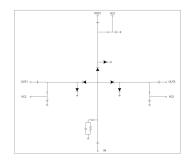
P-1dB: 28dBm @17GHz50Ohm input / output

100% on-wafer testing

Chip size: 2.12 x 1.77 x 0.1mm

Silicon nitride passivation, scratch protection

### Functional Block Diagram



#### **Product Introduction**

GSW310E is a GaAs PIN reflective single-pole triple-throw switch chip with  $50\Omega$  matching at the input/output end, a frequency range of 2 to 20GHz, and -5V/+5V control. It has a built-in bias network and DC blocking capacitors, which is easy to use. It has excellent switching characteristics and port standing wave characteristics in the entire operating frequency range, and is very suitable for microwave hybrid integrated circuits, multi-chip modules, and low-power systems. The switch chip uses on-chip through-hole metallization technology to ensure good grounding, does not require additional grounding measures, and is simple and convenient to use. The back of the chip is metallized, which is suitable for eutectic sintering or conductive adhesive bonding processes.

Use restriction parameter <sup>1</sup>		
Maximum input voltage	2 5V	
Maximum input power	+33dBm CW	
Operating temperature	-55 ~ +85°C	
storage temperature	-65 ~ +150°C	

[1] Exceeding any of these maximum limits may cause permanent damage.

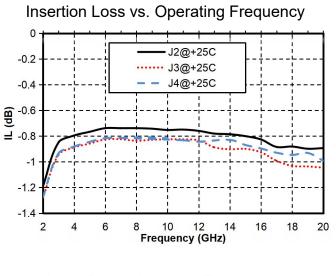
Electrical performance parameters (TA = +25°C)					
index	Minimum	Typical Value	Maximum	unit	
Frequency Range		2-20	GHz		
Insertion loss	-	0.8	1.2	dB	
Isolation	39	48	-	dB	
Input return loss	19	23	-	dB	
Output return loss	17	21	-	dB	
P-1dB @17GHz	-	28	-	dBm	
Switching speed	-	20	-	ns	

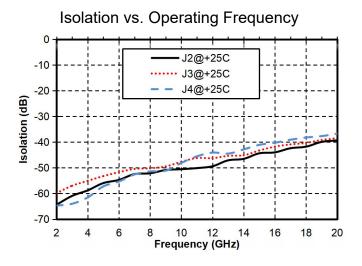
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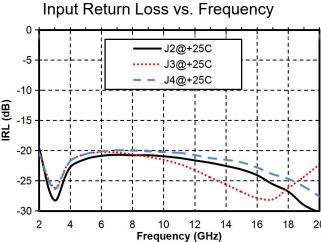


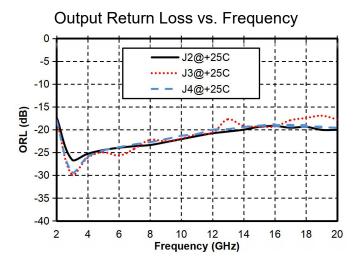
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#### Main index test curve









# **Typical Driver Connections**

CON	TROL LEVE	L (DC	F	RF OUTPUT STATE	<b>=</b>
CURRENT)					
VC1	VC2	VC3	OUT1(J2)-IN(J1)	OUT2(J3)-IN(J1)	OUT3(J4)-IN(J1)
-10mA	+20mA	+20mA	Low Loss	Isolation	Isolation
+20mA	-10mA	+20mA	Isolation	Low Loss	Isolation
+20mA	+20mA	-10mA	Isolation	Isolation	Low Loss

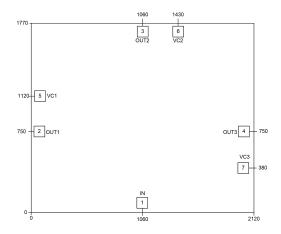
Note: V = +1.35V, I = +20mA; V = -3.2V, I = -10mA (including the on-chip resistor RIN = 50 ohm voltage divider at J1)

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### Appearance structure

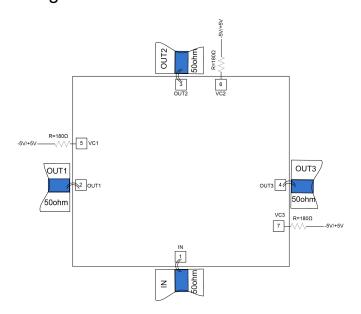


All units in the figure are micrometers

## Bonding point definition

Bonding point	Function Symbol	Functional Description	
number			
1	IN(J1)	RF input signal terminal	
2, 3, 4	OUT1(J2), OUT2(J3), OUT3(J4)	RF output signal terminal	
5, 6, 7	VC1, VC2, VC3	Signal control port	
Chip bottom	GND	The bottom of the chip needs to be well	
	GND	grounded to RF and DC	

### Recommended circuit diagram



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+5V is connected in series with R=180 ohm resistor , V =+ 1.35V , I=+20mA . -5V is connected in series with R=180 ohm resistor , V=-3.2V, I=-10mA .

Note: If you need to reduce power consumption, you need to connect +-5V /-5V in series with resistors of different resistance values. For example: +5V in series with R=370 ohm resistor: V=+1.3V, I=+10mA; -5V in series with R=180 ohm resistor V=-3.2V, I=-10mA. (Including the internal resistor RIN=50 ohm voltage divider at J1 end). Please contact the manufacturer for specific usage.

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