

GaAs PIN Reflective Single-pole Double-throw Switch Chip, 2-20GHz

Performance characteristics

Frequency Range: 2 - 20 GHzInsertion loss: 0.9dB typ.

Isolation: 44 dB typ.

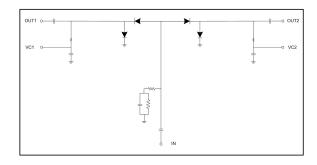
P-1dB: 23dBm @17GHz50Ohm input / output

100% on-wafer testing

• Chip size: 1.92 x 1.11 x 0.1mm

• Silicon nitride passivation, scratch protection

Functional Block Diagram



Product Introduction

GSW2B1 is a GaAs PIN reflective single-pole double-throw switch chip with 50Ω matching at the input/output end, a frequency range of 2 to 20GHz, and -5V/+5V control. It has a built-in bias network and DC blocking capacitors, which is easy to use. It has excellent switching characteristics and port standing wave characteristics in the entire operating frequency range, and is very suitable for microwave hybrid integrated circuits, multi-chip modules, and low-power systems. The switch chip uses on-chip through-hole metallization technology to ensure good grounding, does not require additional grounding measures, and is simple and convenient to use. The back of the chip is metallized, which is suitable for eutectic sintering or conductive adhesive bonding processes.

Use restriction parameter ¹		
Maximum input voltage	2 5V	
Maximum input power	+30dBm CW	
Operating temperature	-55 ~ +85°C	
storage temperature	-65 ~ +150°C	

[1] Exceeding any of these maximum limits may cause permanent damage.

Electrical performance parameters (TA = +25°C)					
Index	Minimum	Typical Value	Maximum	nit	
Frequency Range		2-20		G Hz	
Insertion loss	-	0.9	1.0	dB	
Isolation	38	44	-	dB	
Input return loss	17	19	-	dB	
Output return loss	25	28	-	dB	
P-1dB @17GHz	-	23	-	dBm	
Switching speed	-	20	-	ns	

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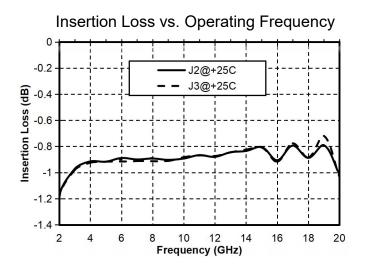
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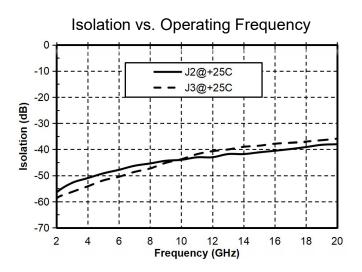
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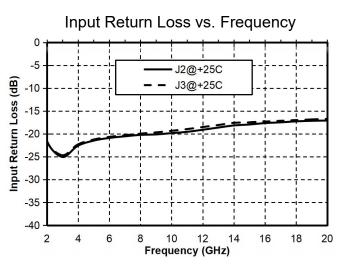


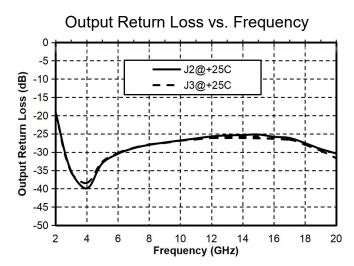
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Main index test curve









Typical Driver Connections

CONTROL LEVEL (DC CURRENT)		RF OUTPUT STATE	
J2	J 3	J2-J1	J3 - J1
-10mA	+1 2mA	Low Loss	Isolation
+ 12mA	-10mA	Isolation	Low Loss

Note: $V \approx +1.32 \text{ V}$, $I \approx +12 \text{ mA}$; $V \approx -1.90 \text{ V}$, $I \approx -10 \text{ mA}$ (including RIN $\approx 50 \text{ ohm resistor}$ voltage divider at J1)

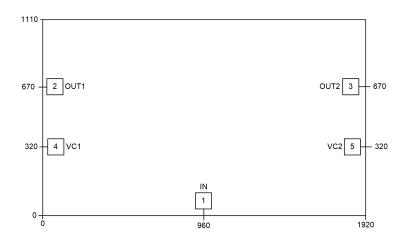
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Appearance structure

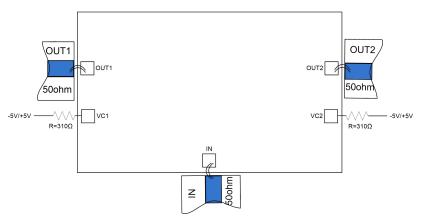


All units in the figure are micrometers

Bonding point definition

Bonding point	Function Symbol	Functional Description
number		
1	IN(J1)	RF input signal terminal
2.3	OUT2(J2), OUT3(J3)	RF output signal terminal
4, 5	VC1, VC2	Control Port
Chip bottom	GND	The bottom of the chip needs to be well grounded to RF and DC

Recommended circuit diagram



+5V series R≈310 ohm resistor, V≈+1.28V , I≈+12mA ; -5V series R≈310 ohm resistor, V≈-1.80V , I≈-10mA . Users can change the resistance value according to their own situation. If you have any questions, please contact the manufacturer.

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