

# GaAs MMIC Absorptive SPST Switch Chip, DC-19GHz

### Performance characteristics

Frequency range: DC -19GHz

• Insertion loss : 0.8 dB

Isolation: 50 dB

Input/output standing wave ratio: 1.1

500hm input / output100% on-wafer testing

• Chip size: 1.76 x 0.92 x 0.1mm

#### **Product Introduction**

GSW-0019ST is a GaAs MMIC absorptive single-pole single-throw switch chip with  $50\Omega$  matching at the input/output ends , a frequency range covering DC ~19GHz , and 0V/-5V power supply. The switching speed is less than 10ns. The chip uses on-chip through-hole metallization technology to ensure good grounding, no additional grounding measures are required, and it is simple and convenient to use. The back of the chip is metallized and is suitable for eutectic sintering or conductive adhesive bonding processes.

Use restriction parameter <sup>1</sup>			
Control voltage range	-8V ∼ +0.5V		
Maximum input power	+30dBm		
Operating temperature	-55 ~ +85°C		
storage temperature	-65 ~ +150°C		

[1] Exceeding any of these maximum limits may cause permanent damage.

Electrical Parameters ( TA = +25°C)					
index	Minimum	Typical Value	Maximum	unit	
Frequency Range		DC-19		G Hz	
Insertion loss	-	0.8	1.2	dB	
Isolation	45	55	-	dB	
On-state input /output return loss (ON)	19/19	22/23	-	dB	
P-1dB@18GHz		26		dBm	
Switching speed		10		ns	

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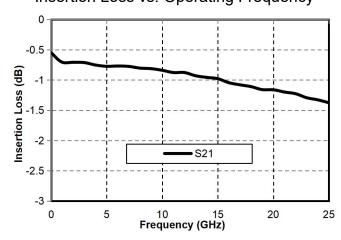
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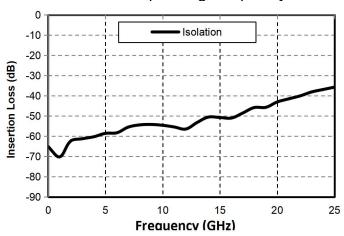
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### Main index test curve

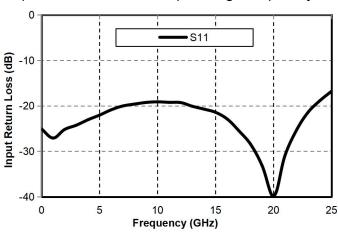
Insertion Loss vs. Operating Frequency



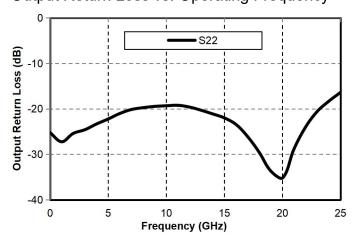
Isolation vs. Operating Frequency



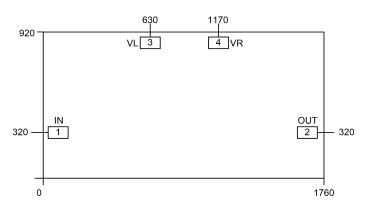
Input Return Loss vs. Operating Frequency



Output Return Loss vs. Operating Frequency



## Appearance structure <sup>2</sup>



[ 2 ] All units in the figure are micrometers



# **GSW-0019ST**

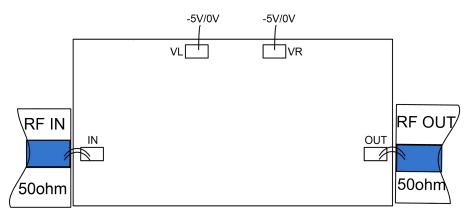
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Bonding point definition				
Bonding point	Function	Functional Description		
number	Symbol			
1	RFIN	The signal input terminal is connected to a 50 ohm circuit, and there		
		is no DC blocking capacitor integrated into the chip.		
2	RF OUT	The signal output terminal is connected to a 50 ohm circuit, and		
		there is no DC blocking capacitor integrated into the chip.		
3.4	Voltage Control	On/off control		
Chip bottom	GND	The bottom of the chip needs to be well grounded to RF and DC		

## Truth table

V L	VR	state
0V	-5V	RF1-RF2 conduction
-5V	0V	RF1-RF2 shutdown

## Recommended assembly drawing



- 1. No external resistor is required for the control pad.
- 2. It is recommended to bond two gold wires with a length of 300um and a diameter of 25um at each input and output port.

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