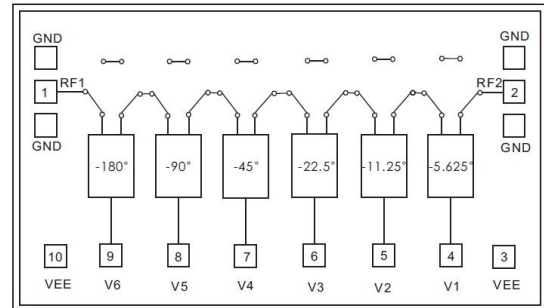


GaAs MMIC 6-bit digitally controlled phase shifter chip , 0.9-1.3GHz

Performance characteristics

- Frequency range: 0.9 - 1.3 GHz
- Insertion loss: 4.7dB (Typ.)
- Insertion loss fluctuation : 1.1 dB
- Phase shift accuracy (RMS): 1.3 °
- Integrated control logic
- 50Ohm input / output
- 100% on-wafer testing
- Chip size 3.92 x 1.88 x 0.1mm

Block Diagram



Product Introduction

GPS-009013-6B-PD is a GaAs MMIC 6-bit digitally controlled phase shifter chip with a frequency range of 0.9 GHz to 1.3 GHz , an insertion loss of 4.7 dB , and a phase shift accuracy of 1.3 ° . The chip is powered by -5V and controlled by 0/+5V logic. The chip uses on-chip through-hole metallization technology to ensure good grounding, and does not require additional grounding measures. It is simple and convenient to use. The back of the chip is metallized and is suitable for eutectic sintering or conductive adhesive bonding processes.

Use restriction parameter ¹

Maximum input power	+25dBm
Control voltage range	-0.5V ~ + 6V
Supply voltage range	- 6V
Operating temperature	-55 ~ +85°C
Storage temperature	-65 ~ +150°C

【1】 Exceeding any of these maximum limits may cause permanent damage.

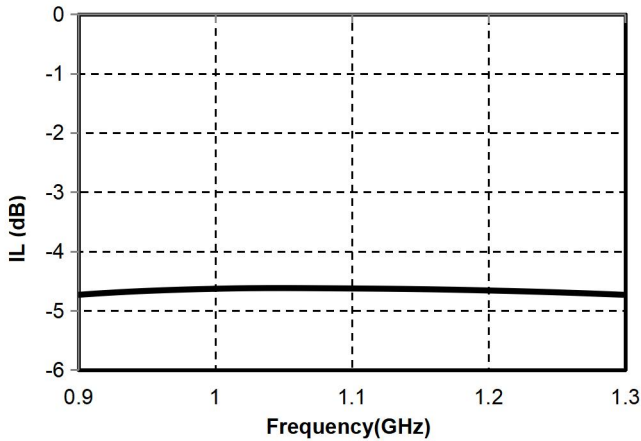
Electrical performance parameters (TA = +25°C)

Index	Minimum	Typical Value	Maximum	Unit
Frequency Range	0.9-1.3			GHz
Insertion loss	-	4.7	-	dB
Insertion loss fluctuation	-	1.1	-	dB
Phase shift accuracy (RMS)	-	1.3	-	degree
Input return loss	-	22	-	dB
Output return loss	-	23	-	dB
Switching time		50		ns
P-1		21		dBm
Control voltage	0	-	+ 5	V
Control current	-	3	-	mA
voltage	-	-5V	-	V
Quiescent Current	-	15	-	mA

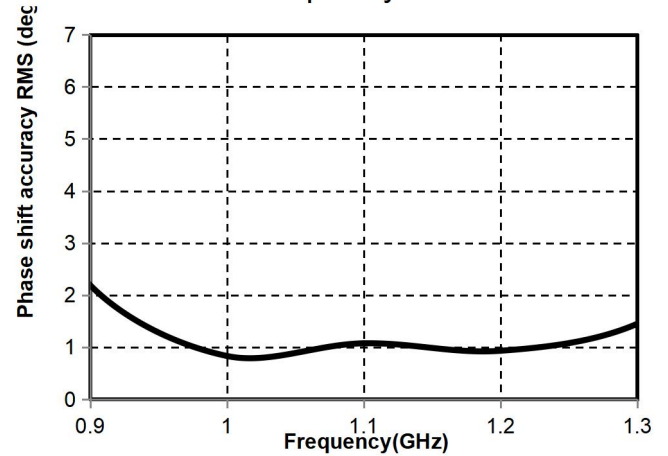
GaAs MMIC 6-bit Digitally Controlled Phase Shifter Chip , 0.9-1.3GHz

Main index test curve

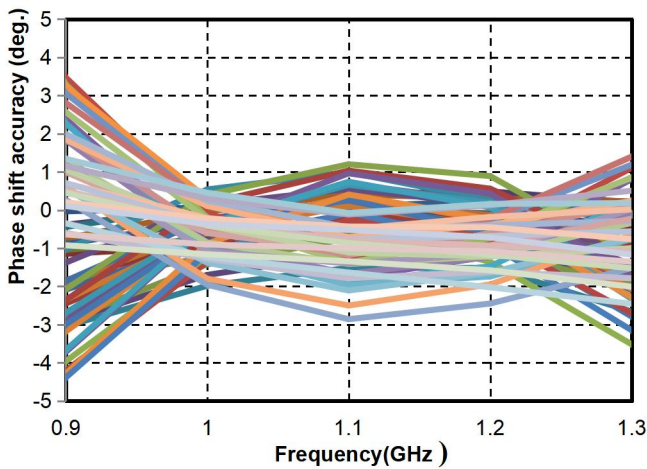
Insertion Loss vs. Operating Frequency



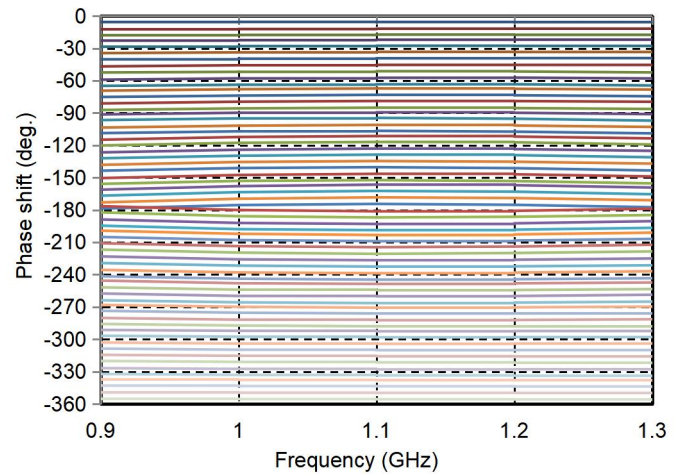
Phase shift accuracy (RMS) vs. operating frequency



Phase shift accuracy (absolute value) vs. operating frequency

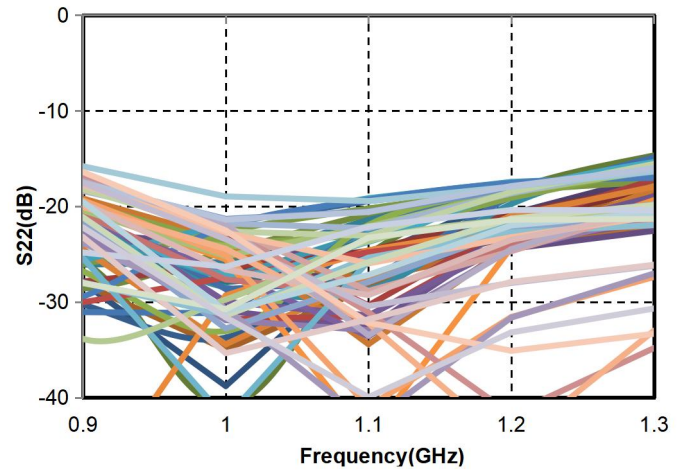
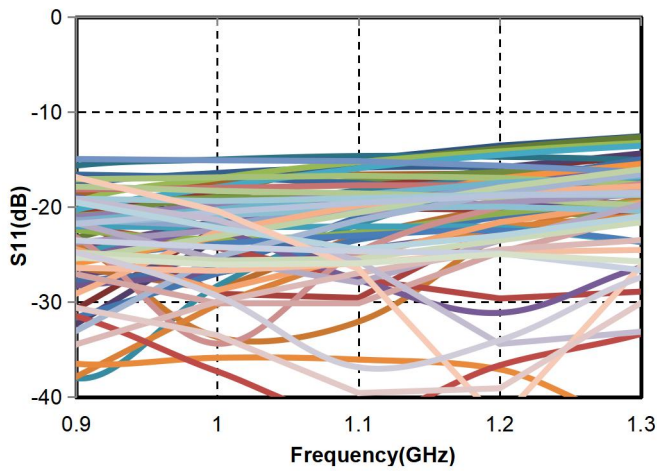


Phase shift vs. operating frequency

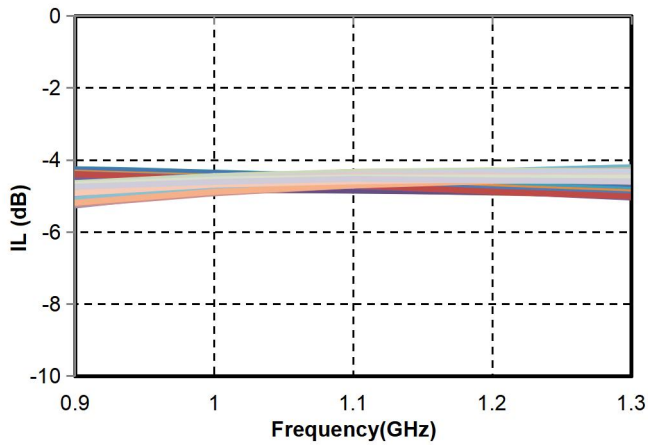


Full-state phase shift input return loss vs. operating frequency

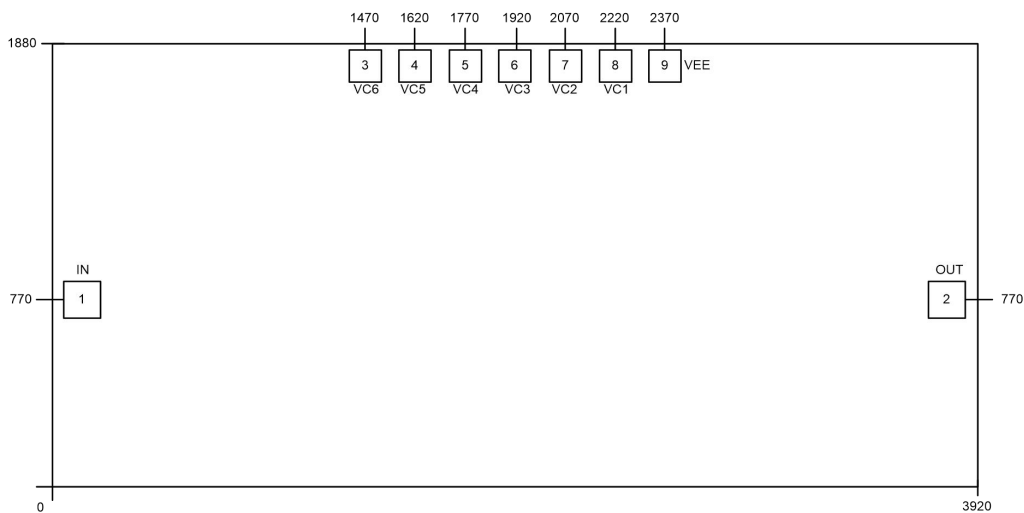
Full-state phase shift output return loss vs. operating frequency



Full-state phase shift insertion loss vs. operating frequency



Appearance structure ²



【 2 】 The units in the figure are all micrometers (dimensional tolerance: ±100um.)

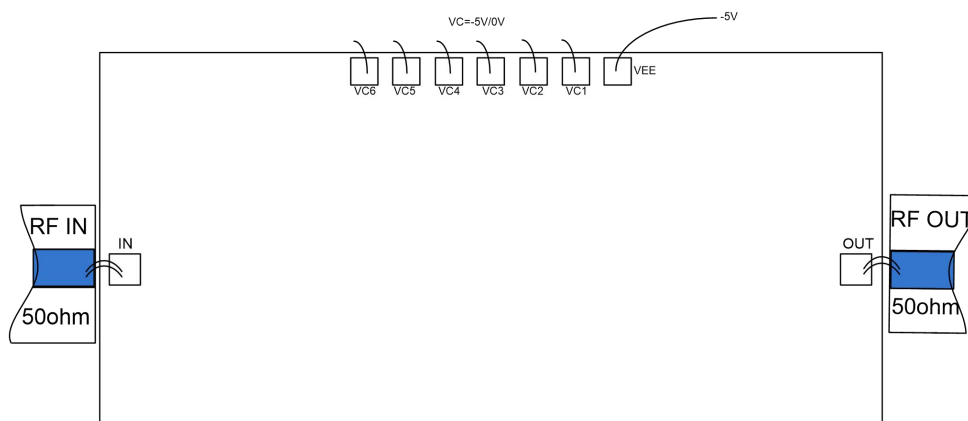
Bonding point definition

Bonding point number	Function Symbol	Functional Description
1	RF IN	RF signal input terminal
2	RF OUT	RF signal output terminal
9	VEE	Chip power supply
3~8	CTRL	Control Port
Chip bottom	GND	The bottom of the chip needs to be well grounded to RF and DC

Truth Table

	VEE	VC6	VC5	VC4	VC3	VC2	VC1
0 state	-5	0	0	0	0	0	0
-5.265°		0	0	0	0	0	5
-11.25°		0	0	0	0	5	0
-22.5°		0	0	0	5	0	0
-45°		0	0	5	0	0	0
-90°		0	5	0	0	0	0
-180°		5	0	0	0	0	0
-354.375°		5	5	5	5	5	5

Recommended assembly drawing



Precautions for use

- The chip needs to be stored in an anti-static container and kept in a nitrogen environment.
- bare die surface using wet chemical methods .
- Please strictly follow the ESD protection requirements to avoid static damage to the bare chip.
- General operation: Please use precision pointed tweezers to pick up bare chips. Avoid touching the chip surface with tools or fingers during operation.

- Rack mounting operation suggestions: Bare chip mounting can be done by AuSn solder eutectic sintering or conductive adhesive bonding. The mounting surface must be clean and flat.
- Sintering process: It is recommended to use AuSn solder sheets with a gold -tin ratio of 80/20 . The working surface temperature reaches 255 °C and the tool (vacuum chuck) temperature reaches 265 °C . When the high-temperature mixed gas (nitrogen-hydrogen ratio of 90/10) is blown to the chip, the temperature at the top of the tool should be raised to 290 °C . Do not let the chip exceed 320 °C for more than 20 seconds. The friction time should not exceed 3 seconds.
- Bonding process: The amount of conductive glue dispensed should be as small as possible. After the chip is placed in the installation position , the conductive glue should be vaguely visible around it . For curing conditions, please follow the information provided by the conductive glue manufacturer.
- Bonding operation suggestions: Use $\Phi 0.025\text{mm}$ (1mil) gold wire for both ball and wedge bonding . Thermo-ultrasonic bonding temperature is 150 °C. The pressure of the wedge for ball bonding is 40~50gf , and the pressure of the wedge bonding is 18~22gf . Use the smallest possible ultrasonic energy. The bonding starts at the pressure point on the chip and ends at the package (or substrate) .