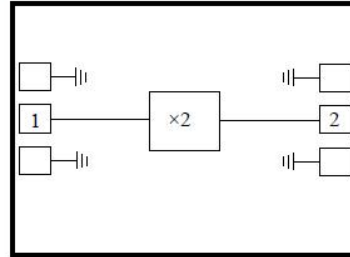


GaAs MMIC Frequency Multiplier Chip, 7-16.5GHz

Performance characteristics

- Input frequency range: 7 - 16.5 GHz
- Output frequency range: 14-33GHz
- Typical input power : 15dBm
- Typical output power: 2.5dBm
- 50Ohm input/output
- 100% on-chip testing
- Chip size : 1.53 x 1.23 x 0.1mm

Functional Block Diagram



Product Introduction

GPL- 0716-2A is a passive doubler chip. When the input signal power is + 15 dBm, the output signal power in the range of 14 GHz ~ 33 GHz is 2.5 dBm. The chip through-hole metallization process ensures good grounding, and the back side is metallized, which is suitable for eutectic sintering or conductive adhesive bonding process .

Use restriction parameter ¹

Maximum input power	+22dBm
Operating temperature	-55 ~ +85°C
storage temperature	-65 ~ +150°C

【1】 Exceeding any of these maximum limits may cause permanent damage.

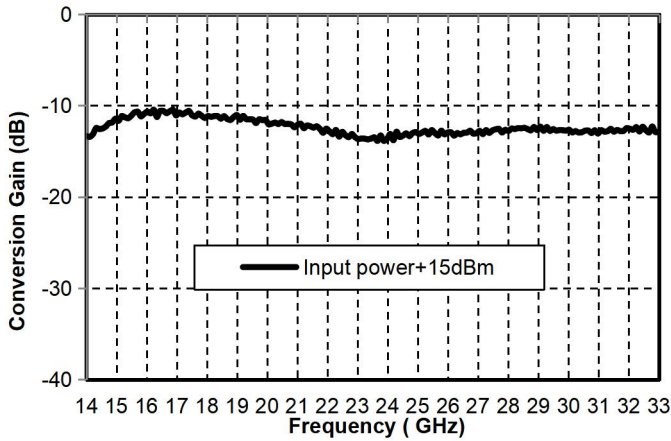
Electrical performance parameters (TA = +25°C , Input Power = +15dBm)

index	Minimum	Typical Value	Maximum	unit
Input frequency range	7-16.5			GHz
Output frequency range	14-33			GHz
Conversion Gain	-	-12	-	dB
Output Power	-	2.5	-	dBm
Fundamental suppression (relative to double frequency output power)	-	36	-	dBc
Third harmonic suppression (relative to doubled frequency output power)	-	31	-	dBc
Input return loss	-	17	-	dB
Output return loss	-	4	-	dB

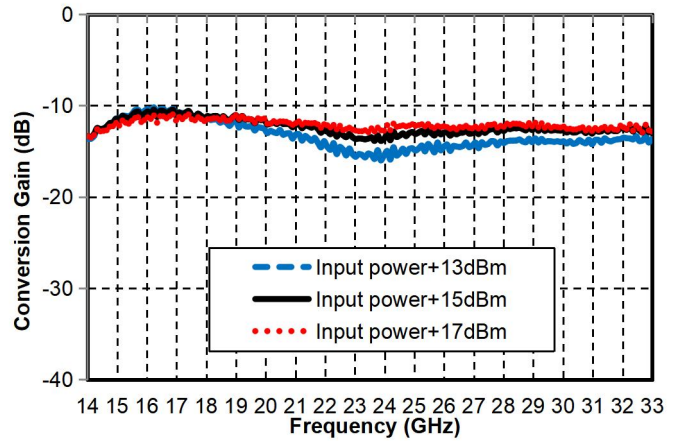
GaAs MMIC Frequency Multiplier Chip, 7-16.5GHz

Main index test curve

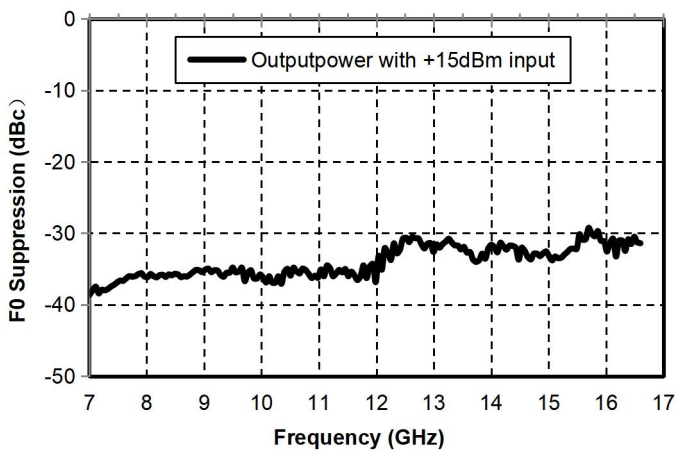
Conversion Gain vs. Frequency



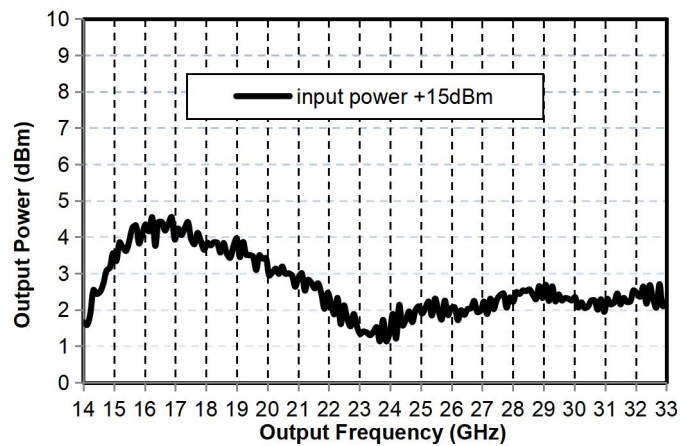
Conversion Gain vs. Input Power



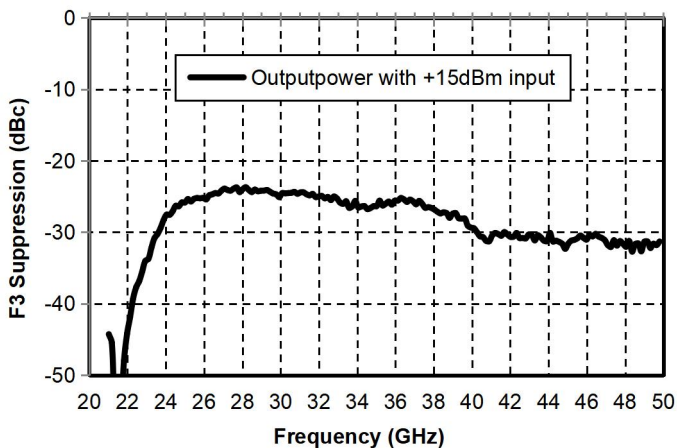
Fundamental Suppression vs. Frequency



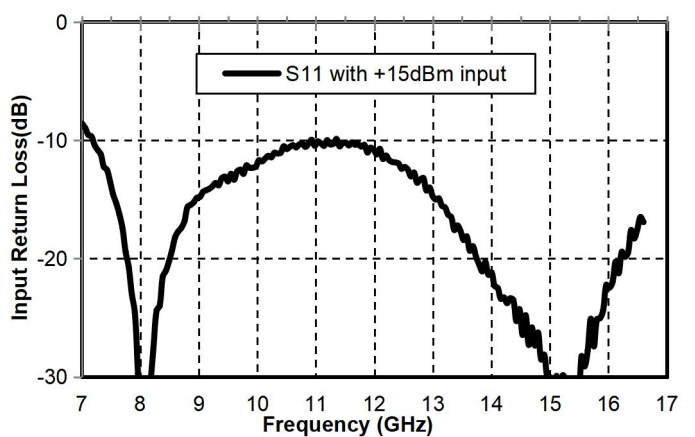
Output Power vs. Frequency



Third Harmonic Suppression vs. Frequency

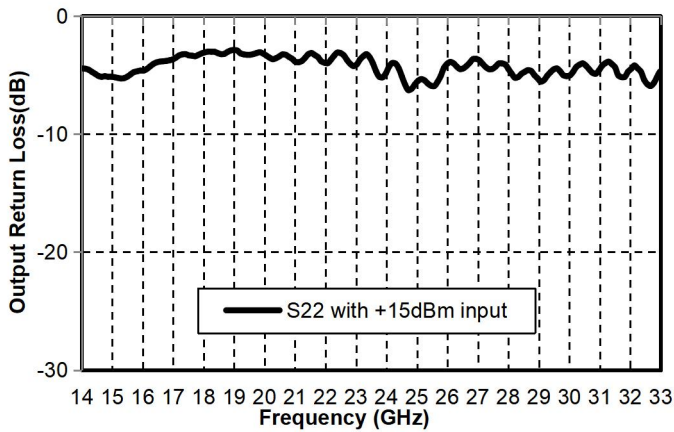


Input Return Loss vs. Frequency



GaAs MMIC passive frequency doubler chip, 7 GHz ~16.5 GHz

Output Return Loss vs. Frequency



Appearance structure	Recommended assembly drawing
<p>The units in the figure are all microns , and the dimensional tolerance is $\pm 50\mu\text{m}$</p>	

Bonding point definition		
Bonding point number	Function Symbol	Functional Description
1	RFIN	RF signal input terminal
2	RF OUT	RF signal output terminal
Chip bottom	GND	The bottom of the chip needs to be well grounded to RF and DC