

## GaAs MMIC Power Amplifier Chip, 32-38GHz

### Performance characteristics

Frequency range: 32-38GHz

Small Signal Gain: 18 dB

Gain flatness :  $\pm 2$ dB

P-1dB: 30.5 dBm

Psat: 31 dBm

Power supply: + 6V@1300mA

50Ohm input/output

100% on-chip testing

Chip size : 2.77 x 2.93 x 0.1mm

### Product Introduction

GPA -3238A is a broadband high-gain, high-efficiency, high- power amplifier chip based on GaAs technology , covering a frequency range of 32~38GHz, with a small signal gain of 18dB and a Psat output power of 31dBm. The amplifier supports +5V operation. The chip via metallization process ensures good grounding, and the back side is metallized for eutectic sintering process.

#### Use restriction parameter <sup>1</sup>

Maximum drain voltage	+9 V
Maximum gate bias	- 3 V
Maximum input power	+25 dBm
Operating temperature	-55 ~ +85°C
Storage temperature	-65 ~ +150°C

【1】 Exceeding any of these maximum limits may cause permanent damage.

#### Electrical parameters (Ta=+25°C, Vd = +6 V, Vg=-0.7V, Ids= 1300 mA)

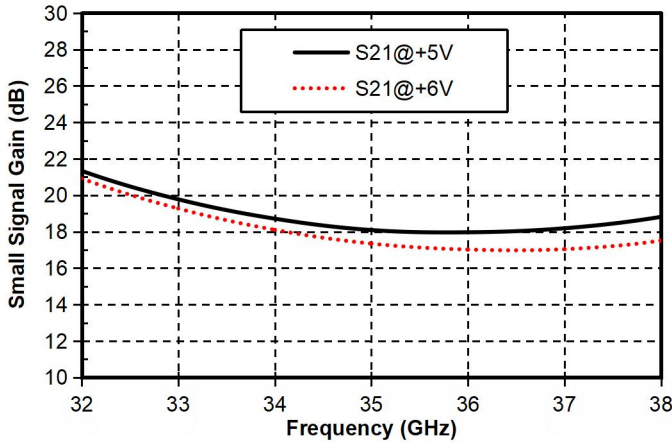
index	Minimum	Typical Value	Maximum	unit
Frequency Range	32-38			GHz
Small Signal Gain	-	18	-	dB
Gain Flatness	$\pm 2.0$			dB
P-1dB	-	30.5	-	dBm
Psat	-	31	-	dBm
Input return loss	-	10	-	dB
Output return loss	-	15	-	dB

\* By tuning the Vg terminal voltage -2V~0V , the recommended gate voltage is -0.7V.

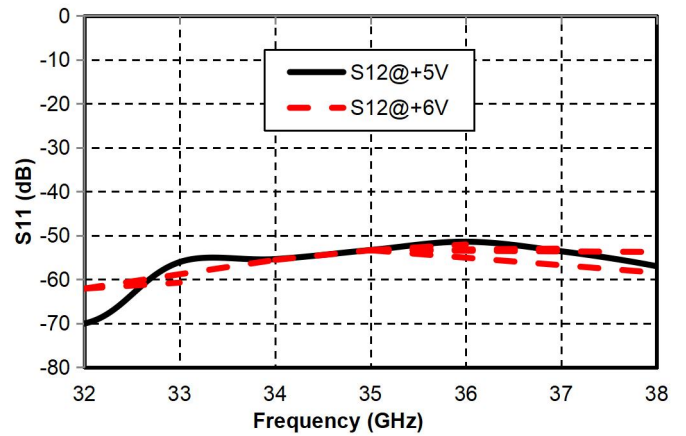
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### Main index test curve

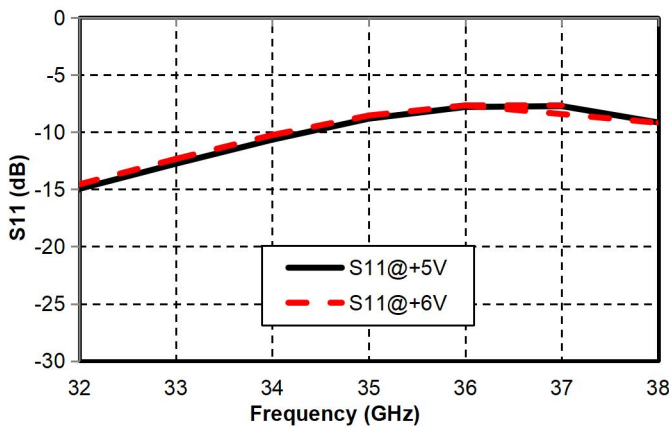
Gain vs. Frequency



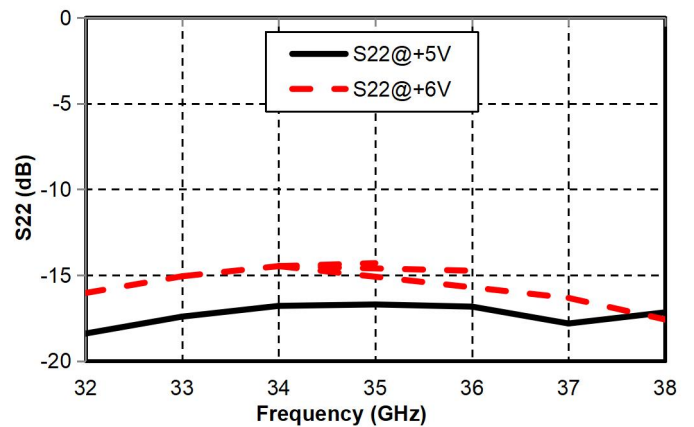
Reverse Isolation vs. Frequency



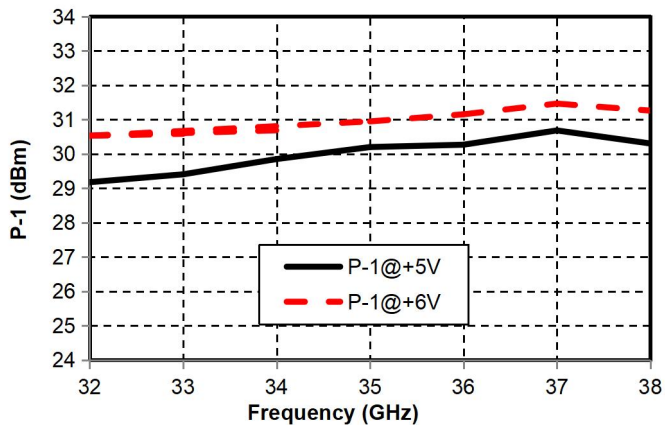
Input Return Loss vs. Frequency



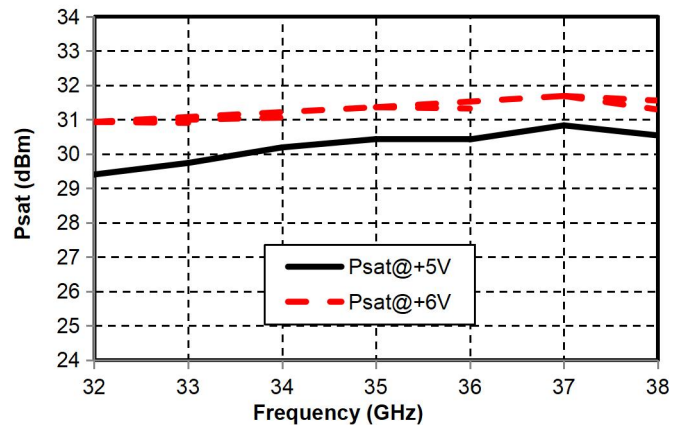
Output Return Loss vs. Frequency



P-1dB vs. Frequency

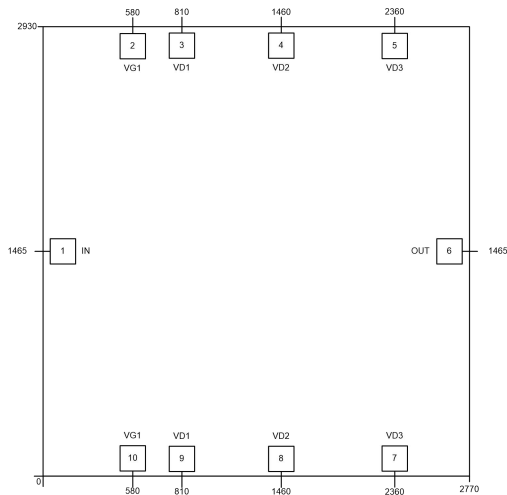


Psat vs. Frequency



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Appearance and structure (unit in the figure is micrometer)



Bonding point definition		
Bonding point number	Function Symbol	Functional Description
1	RF IN	The signal input terminal is connected to a 50 ohm circuit, and no DC blocking capacitor is required.
6	RF OUT	The signal output terminal is connected to a 50 ohm circuit, and no DC blocking capacitor is required.
3, 4, 5, 7, 8, 9	V D1~3	Amplifier drain bias, external 100pF , 1000pF , 4.7uF bypass capacitors are required.
2,10	VG1	Amplifier gate bias , external 100pF , 1000pF , 4.7uF bypass capacitors are required.
6	VG	Amplifier gate bias, external 100pF , 1000pF bypass capacitors are required.
Chip bottom	GND	needs to be in good contact with the RF and DC grounds.

## Recommended assembly diagram

