

GaAs MMIC Power Amplifier Chip, 26-33 GHz

Performance characteristics

- Frequency range: 26 - 33 GHz
- Small signal gain: 27dB
- Gain flatness: ± 0.9 dB
- P -1 dB : 29.5dBm
- Psat : 30dBm
- Power supply: +6 V /450mA
- 50Ohm input / output
- Chip size: QFN 5X5

Product Introduction

GPA-2633A-CQ5B is a broadband amplifier based on GaAs technology , with a frequency range of 26GHz~33GHz, a small signal gain of 27dB, and a Psat output of 30dBm. GPA-2633A-CQ5B is powered by a +6V power supply. This chip uses a 5 x 5 mm ceramic surface mount package , and the surface of the pin pad is gold-plated, which is suitable for reflow soldering installation process.

Using the Limit Parameter

Maximum drain voltage	+9V
Maximum gate bias	-3V
Maximum input power	+25dBm
Operating temperature	-55 ~ +125°C
storage temperature	-65 ~ +150°C

Exceeding any of these maximum limits may cause permanent damage.

Electrical performance parameters (TA = +25°C , Vd = +6V)

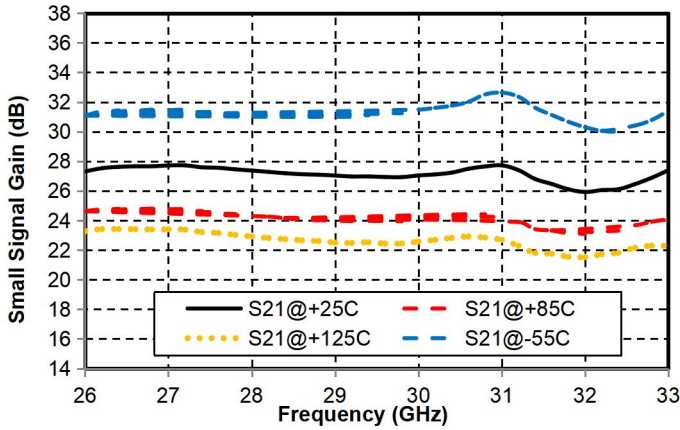
Index	Minimum	Typical Value	Maximum	Unit
Frequency Range	26-33			GHz
Small Signal Gain	-	27	-	dB
Gain Flatness		± 0.9		dB
P -1dB	-	29.5	-	dBm
Psat	-	30	-	dBm
Input return loss	-	12	-	dB
Output return loss	-	13	-	dB
Quiescent Current		450		mA

* By tuning the Vg terminal voltage from -2V to 0V , the recommended Vg terminal voltage is -0.85V .

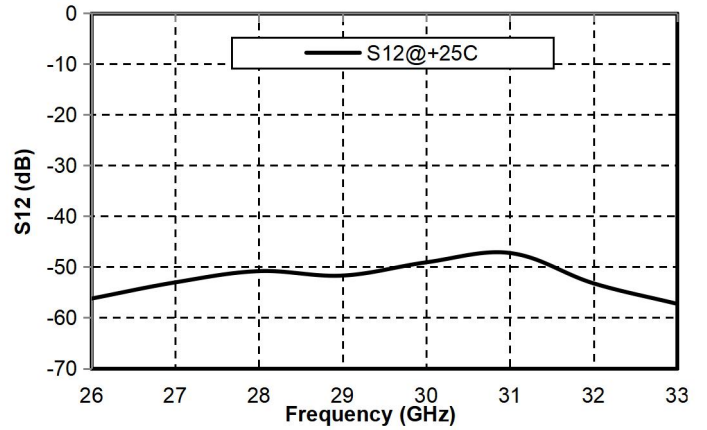
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Main index test curve

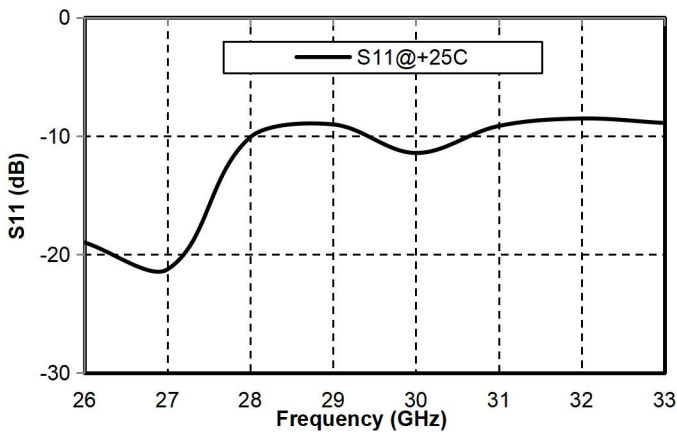
Gain vs. Frequency



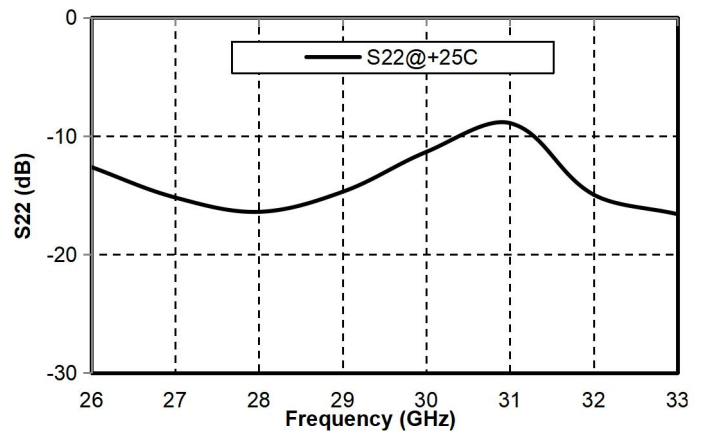
Reverse Isolation vs. Frequency



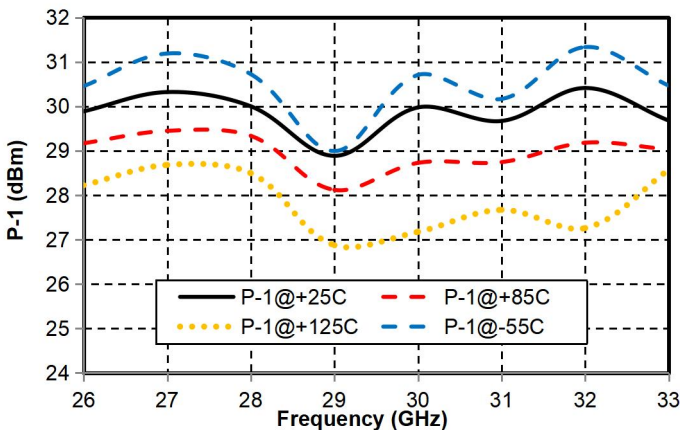
Input Return Loss vs. Frequency



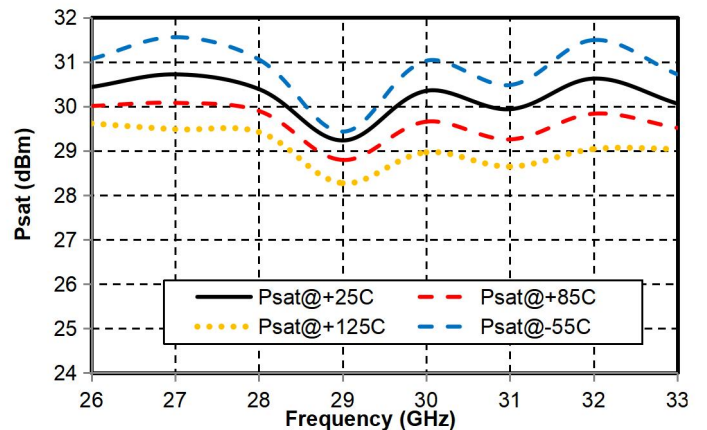
Output Return Loss vs. Frequency



P-1dB vs. Frequency

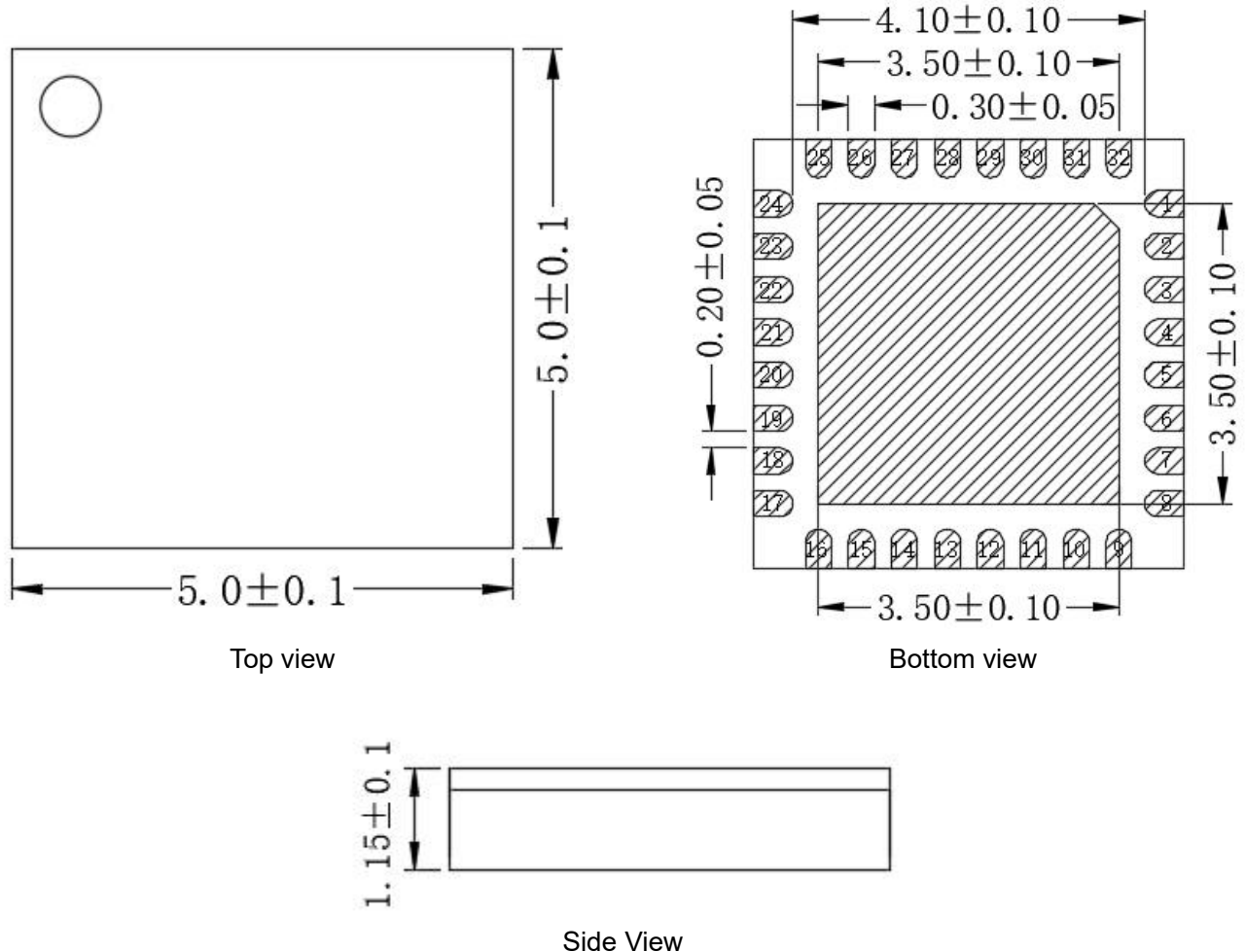


P sat vs. frequency



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Appearance structure



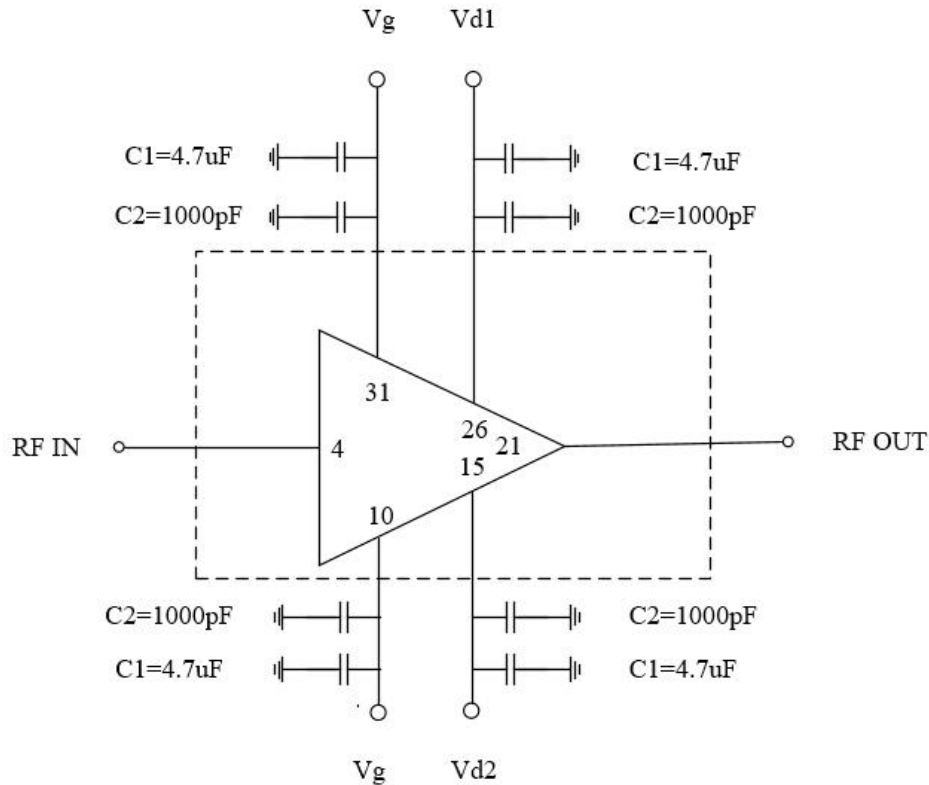
All units in the figures are millimeters .

Pin Definition

Pin number	Function Symbol	Functional Description
4	RFIN	The signal input terminal is connected to a 50 ohm circuit, and no DC blocking capacitor is required
21	RFOUT	The signal output terminal is connected to a 50 ohm circuit, and no DC blocking capacitor is required
10 , 31	VG	Amplifier Gate Bias
15 , 26	VD	Amplifier Drain Bias
1-3 、 5-9 、 16-20 、 22-25 、 32	GND	The pins need to be well grounded to the RF and DC grounds
Chip bottom	GND	The bottom of the chip needs to be well grounded to RF and DC
other	NC	No welding required, can be grounded

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Recommended Circuit



Precautions for use

- Sealing material : Ceramic material that meets ROHS standards
- Lead surface plating: gold, gold layer thickness 0.3um MIN
- Maximum reflow peak temperature: 260 °C