

## GaAs MMIC Mixer Chip, 2.5GHz-7GHz

### Performance characteristics

- RF/LO frequency range: 2.5-7GHz
- IF frequency range: DC-3GHz
- Conversion loss: 7.0dB@13dBm LO input
- LO-RF isolation degree: 54dB
- LO-IF isolation degree: 44dB
- RF-IF isolation degree: 29dB
- Local oscillator power:+11dBm~+15dBm
- Chip size: QFN4x4mm

### Product Introduction

GMX-025070-CQ4 is a GaAs MMIC dual balanced mixer with a frequency range of 2.5GHz~7GHz and an intermediate frequency range of DC~3GHz. The frequency conversion loss is 7.0dB, and the local oscillator/radio frequency isolation is 54dB, 44dB, and 29dB, respectively. The typical local oscillator input power is +13dBm.

RF, LO, and IF ports have no DC blocking capacitors. This mixer adopts a 4X4mm surface mount lead-free ceramic tube shell, and the surface of the pin solder pads is treated with a gold plating process, suitable for reflow soldering installation process.

#### Use restriction parameters<sup>1</sup>

Maximum RF input power	+22dBm
Maximum local oscillator input power	+22dBm
Maximum intermediate frequency input power	+22dBm
Working temperature	-55 ~ +85°C
Storage temperature	-65 ~ +150°C

【1】 Exceeding any of the above maximum limits may result in permanent damage.

#### Electrical performance parameters( $T_A = +25^\circ\text{C}$ , IF=100MHz, LO=+13dBm)

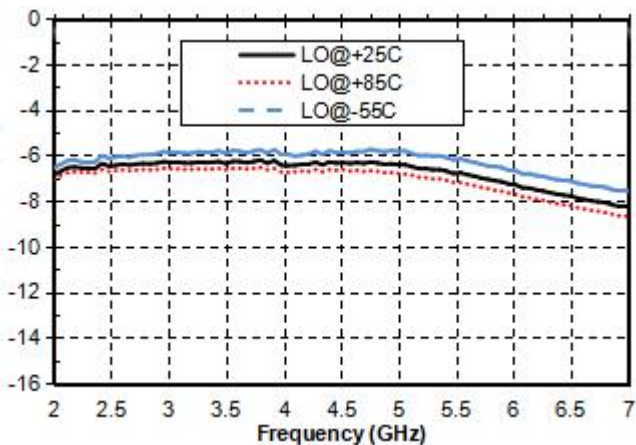
Index	Minimum value	Typical value	Maximum value	Unit
RF frequency range	2.5-7			GHz
Local oscillator frequency range	2.5-7			GHz
Intermediate frequency	DC-3			GHz
Variable frequency loss	-	7.0	-	dB
LO-RF isolation degree	-	54	-	dB
LO-IF isolation	-	44	-	dB

degree				
RF-IF isolation degree	-	29	-	dB
RF input P-1dB		10		dBm
IIP3		18		dBm
The above parameters are all tested in down conversion mode, with an intermediate frequency of 0.1GHz and a local oscillator power of +15dBm.				

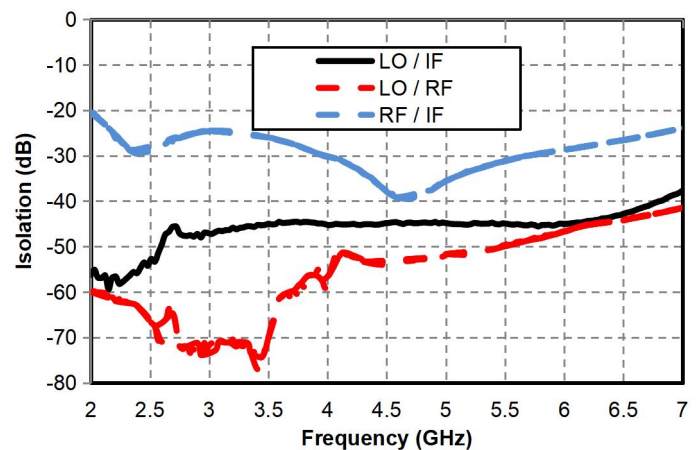
## GaAs MMIC Mixer Chip, 2.5GHz-7GHz

### Main indicator testing curve

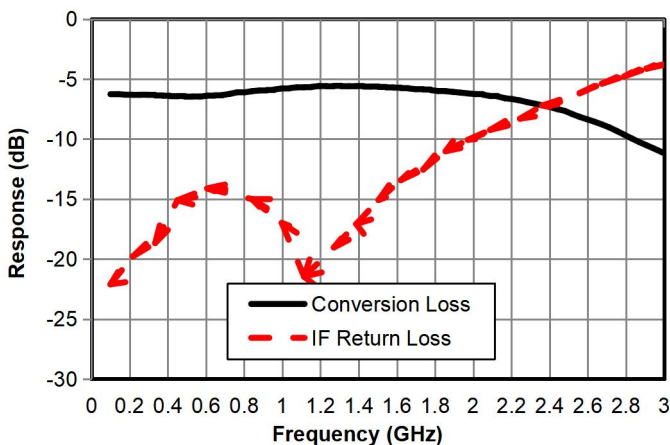
Downconversion loss vs. Temperature @  
LO=+13dBm



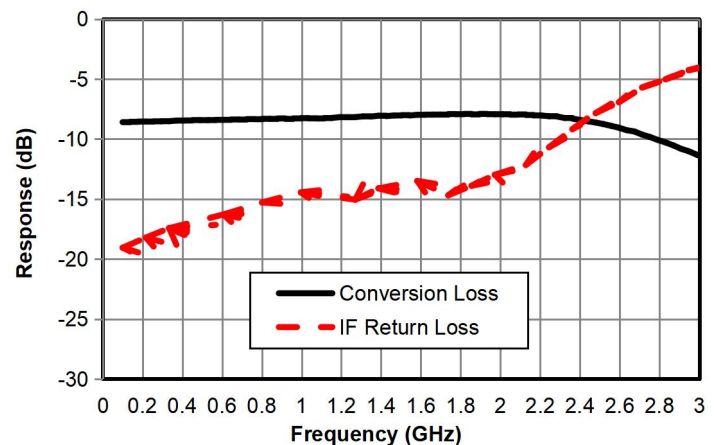
Isolation degree @ LO=+13dBm



Intermediate frequency bandwidth @  
LO=2.5G/+13dBm

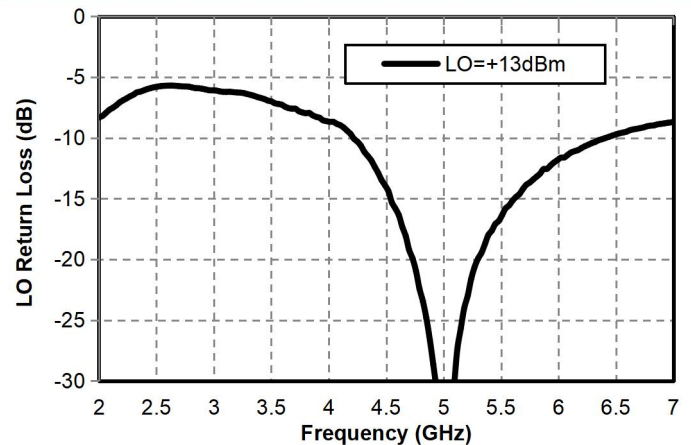
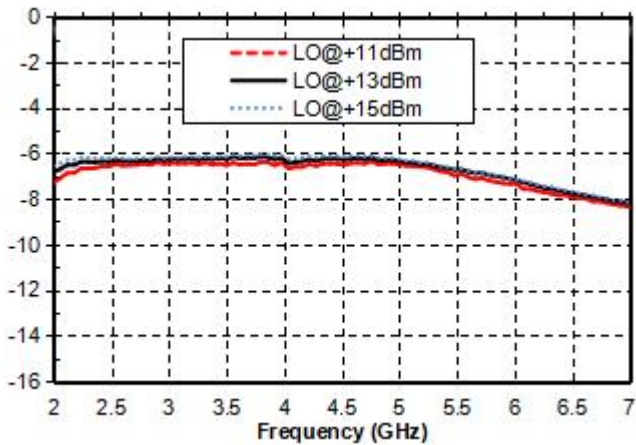


Intermediate frequency bandwidth @  
LO=7G/+13dBm



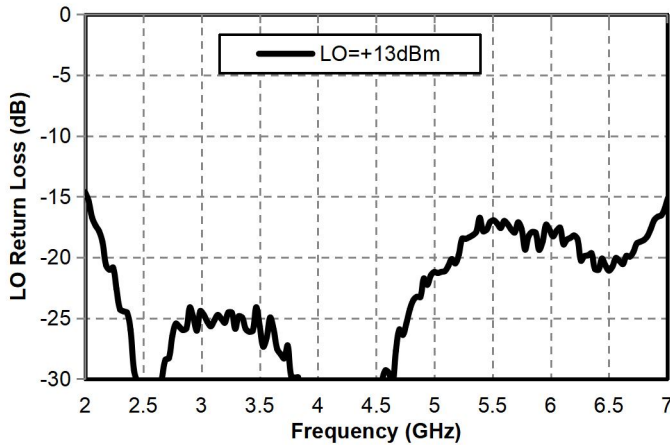
Downconversion frequency conversion loss vs.  
LO power

Downconversion RF return loss vs. frequency  
LO=+13dBm

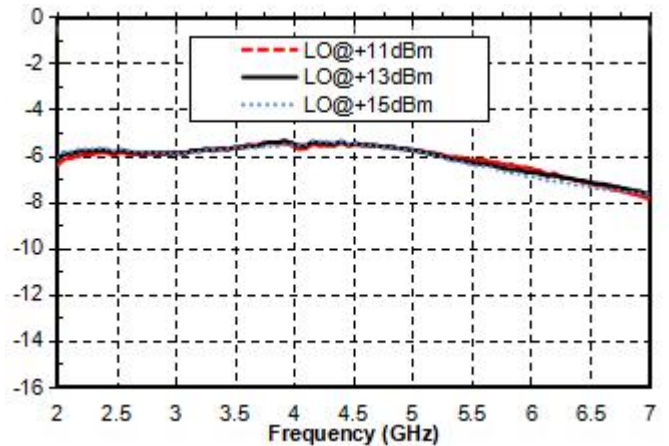


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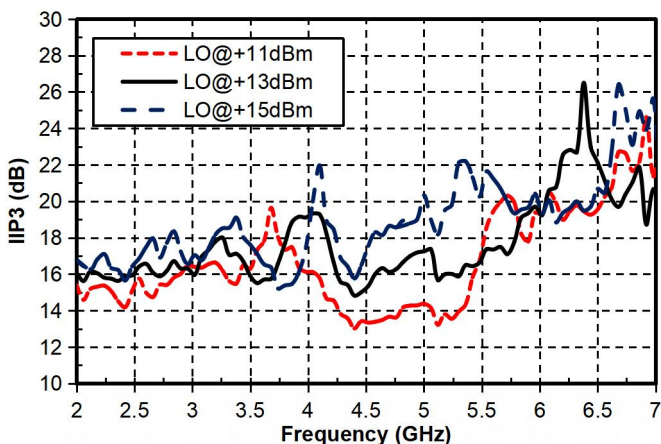
Downconversion local oscillator standing wave vs. frequency



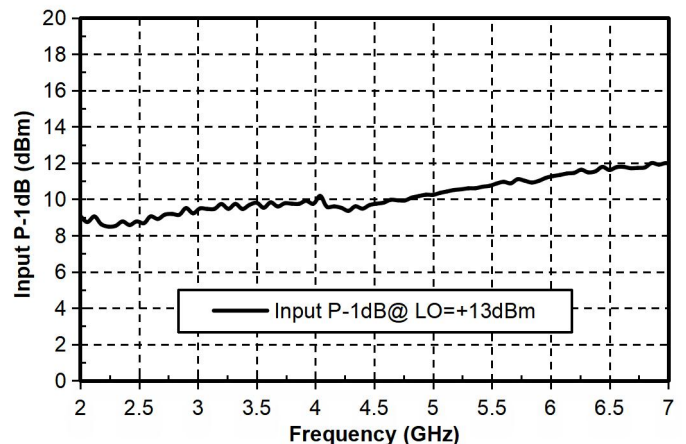
Upconversion loss vs. LO power



IIP3



Input P-1dB vs. RF frequency



## Local oscillator harmonic leakage

LO(GHz)13dBm	nLO (Tested on RF port) dBc	
	2	3
2.50	44	53
3.00	46	56
3.50	45	50
4.00	48	47
4.50	56	47
5.00	56	53
5.50	57	53
6.00	55	55
6.50	54	57
7.00	52	53

## GaAs MMIC Mixer Chip, 2.5GHz-7GHz

### Downconversion combination spurious suppression

Combined Stray Testing	nLO				
	0	1	2	3	4
mRF	0	1	2	3	4
0	xxx	15	22	21	44
1	25	0	43	35	33
2	70	70	78	62	84
3	73	83	81	68	87
4	/	/	/	105	111

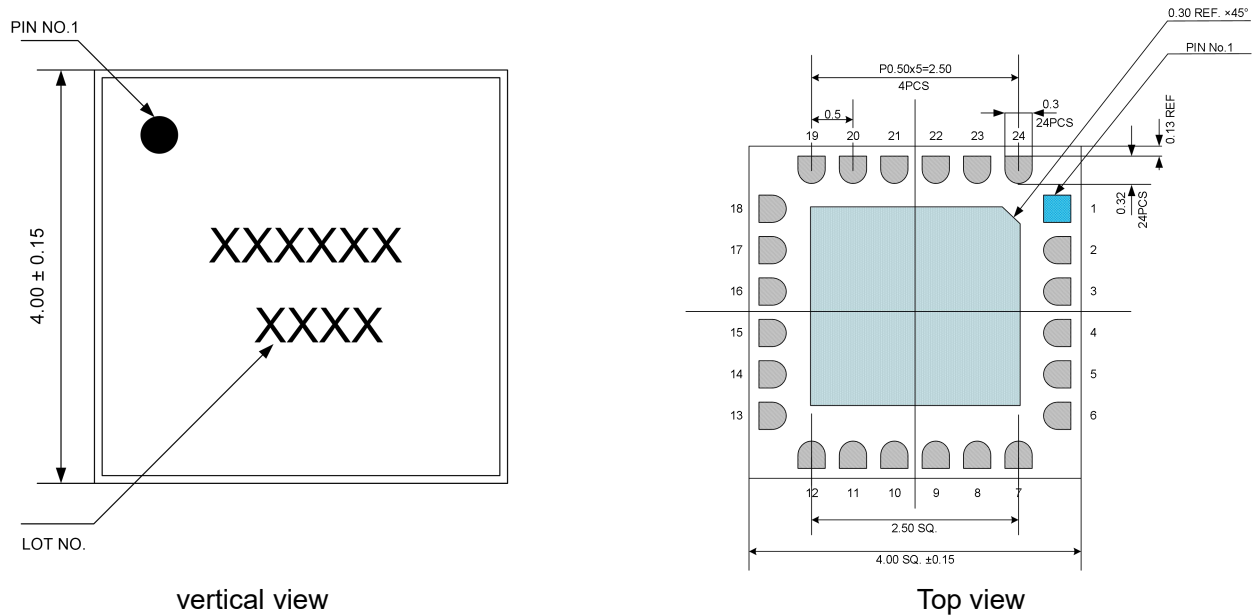
IF= 4.1GHZ@-10dBm , LO= 4GHZ@13dBm The relative value of 1 \* RF-1 \* LO (P\_IF, dBm) is expressed in dBc.

### Upconversion combination stray suppression

Combined Stray Testing	nLO				
	0	1	2	3	4
mIF	0	1	2	3	4
0	xxx	21	28	24	30
1	23	0	38	31	52
2	64	46	81	71	73
3	89	69	90	89	99
4	109	96	/	/	/

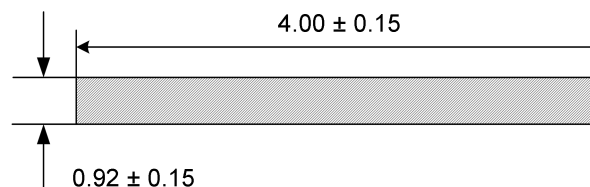
IF= 0.3GHZ@-10dBm , LO= 5GHZ@13dBm The relative value of 1 \* RF-1 \* LO (P\_IF, dBm) is expressed in dBc.

## External structure



## GaAs MMIC Mixer Chip, 1GHz-4GHz

## External structure



Side view

The units in the figure are all millimeters, with an unspecified tolerance of  $\pm 0.15$ mm

## Pin Definition

Solder joint serial number	Functional symbols	Function Description
3	RF	RF signal terminal requires an additional DC isolation capacitor
9	IF	Intermediate frequency signal terminal requires an additional DC isolation capacitor
16	LO	The local oscillator signal terminal requires an additional DC isolation capacitor
2、4、8、10、15、17	GND	The pins should have sufficient and good contact with the RF and DC ground
Chip bottom	GND	The bottom of the chip needs to be well grounded with RF

		and DC
other	NC	No welding required

## Application Block Diagram

