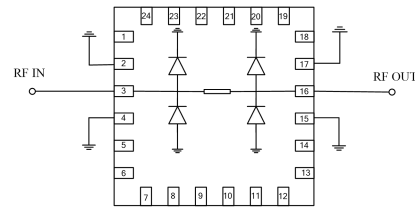


GaAs MMIC Limiter Chip, 1 - 6 GHz

Performance characteristics

- Frequency Range: 1 - 6 GHz
- Insertion loss : 0.35 dB (Tpy .)
- Limiting level: 13dB m
- Power handling: 46dBm (CW)
- 50Ohm input / output
- Chip size: QFN 4X4

Functional Block Diagram



GLM-0 106A-PQ4 is a GaAs MMIC limiter with a frequency range of 1-6 GHz, 50Ω input/output, 0.6 dB within the operating frequency band, 1.3 input and output standing wave , and 46 dBm (CW) burnout resistance . This chip is packaged in a 4x4mm plastic surface mount package, and the surface of the pin pad is gold-plated, which is suitable for reflow soldering installation.

Use limit parameters (exceeding any of the above maximum limits may cause permanent damage)

Maximum input power	+46dBm
Operating temperature	-55 ~ + 125 °C
Storage temperature	-65 ~ +150°C

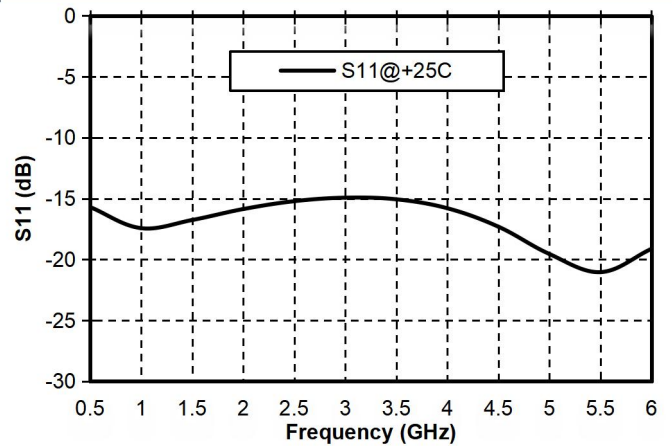
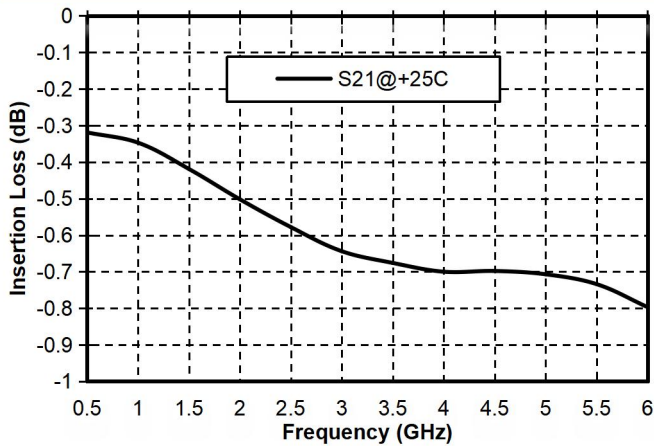
Electrical performance parameters (TA = +25°C)

Index	Minimum	Typical Value	Maximum	Unit
Frequency Range	1-6			G Hz
Insertion loss	-	0.6	-	dB
Input return loss	-	17	-	dB
Output return loss	-	18	-	dB
Clipping level	-	15	-	dBm
Anti-burning power	-	-	46	dBm

Main index test curve

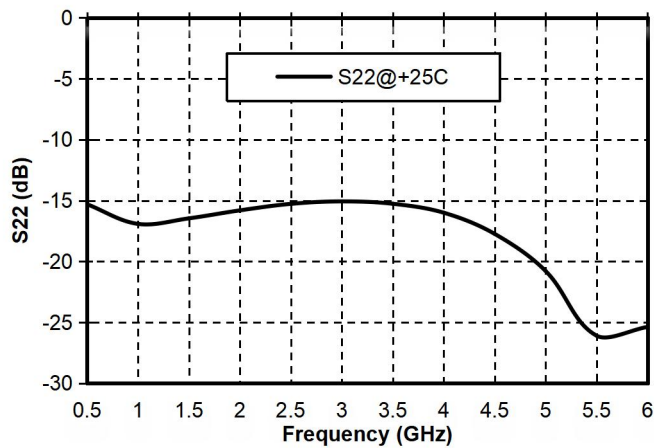
Insertion Loss vs. Operating Frequency

Input Standing Wave vs. Operating Frequency

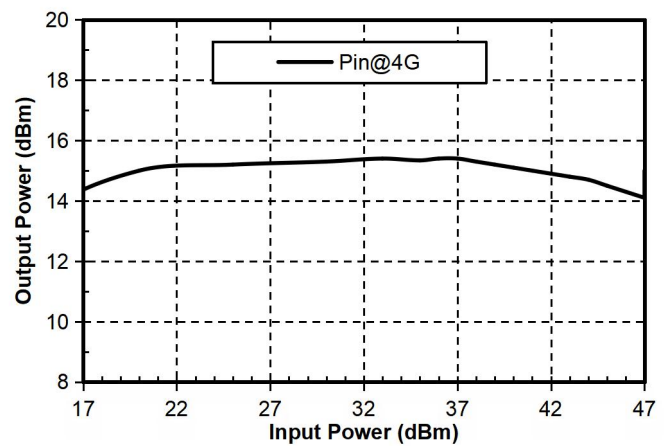


GaAs MMIC Limiter Chip, 1 - 6 GHz

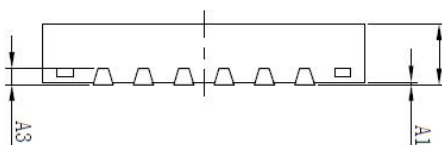
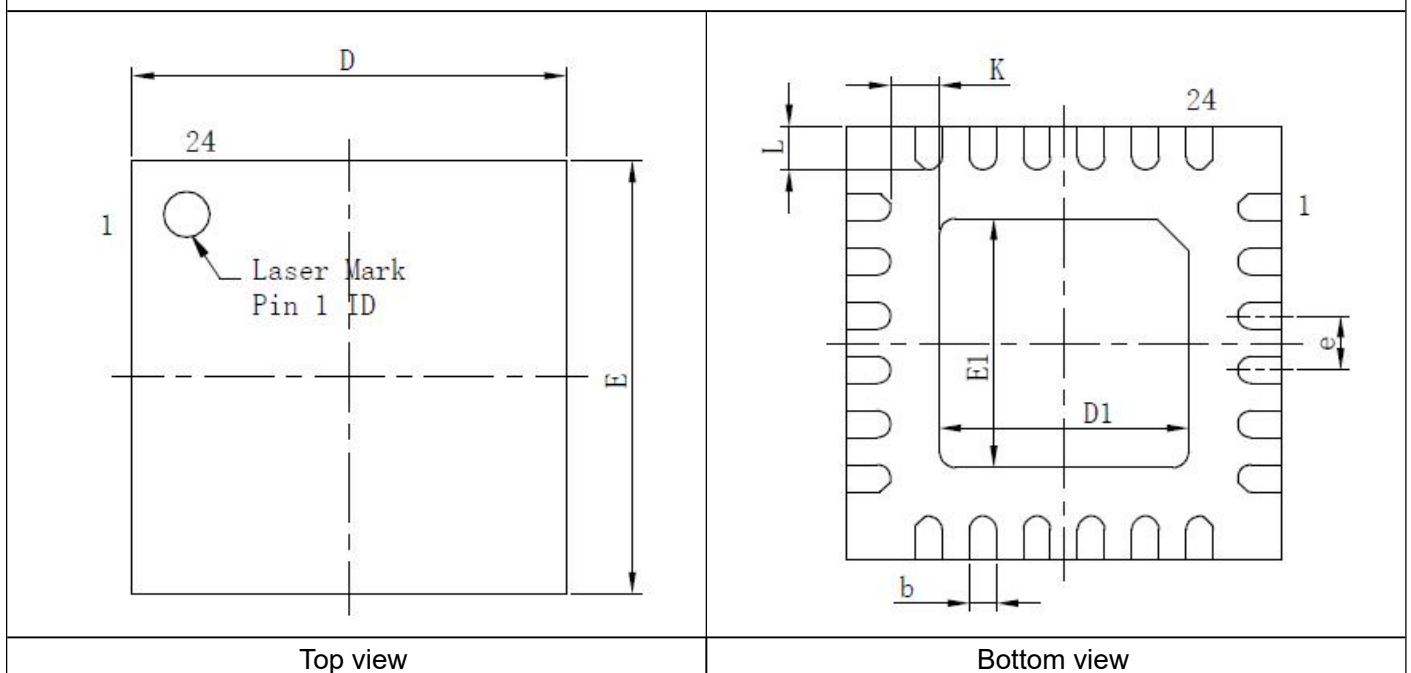
Output Standing Wave vs. Operating Frequency



Clipping level@4G



Appearance structure



Side View
All units in the figures are millimeters .

GaAs MMIC Limiter Chip, 0.5 - 4 GHz

Structure size							
Annotation	Minimum	Standard	Maximum	Annotation	Minimum	Standard	Maximum
A	0.70	0.75	0.80	D1	2 . 2 0	2.30	2.40
A1	0.00	-	0.05	E1	2 . 2 0	2.30	2.40
A3	0.203REF			e	0.5TYP		
b	0.20	0.25	0.30	K	0.20	-	-
D	3.90	4.00	4.10	L	0.30	0.40	0.50
E	3.90	4.00	4.10				

All units in the figures are millimeters .

Pin Definition		
Bonding point number	Function Symbol	Functional Description
3	RFIN	RF signal input terminal. The package input terminal does not integrate a DC blocking capacitor . It is recommended to connect an external DC blocking capacitor.
16	RFOUT	RF signal output terminal, DC blocking capacitor integrated into the input terminal
2, 4, 15, 17	GND	The bottom of the chip needs to be well grounded to RF and DC
1, 5~14, 18~24	NC	No welding required

Precautions for use

- Sealing material : Low-pressure injection molding plastic that meets ROHS specifications
- Lead frame material: copper alloy
- Lead surface plating: 100% matte tin
- Maximum reflow peak temperature: 260 °C