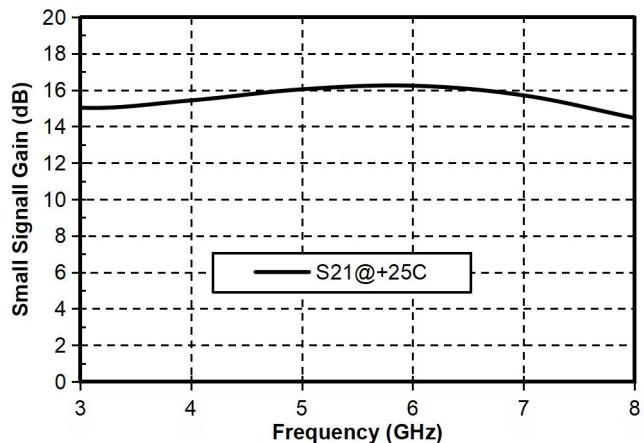


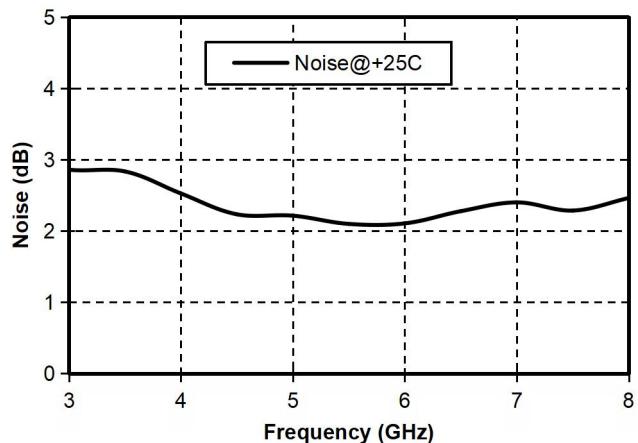
GaAs MMIC Low Noise Amplifier Chip, 3-7GHz

Main indicator testing curve

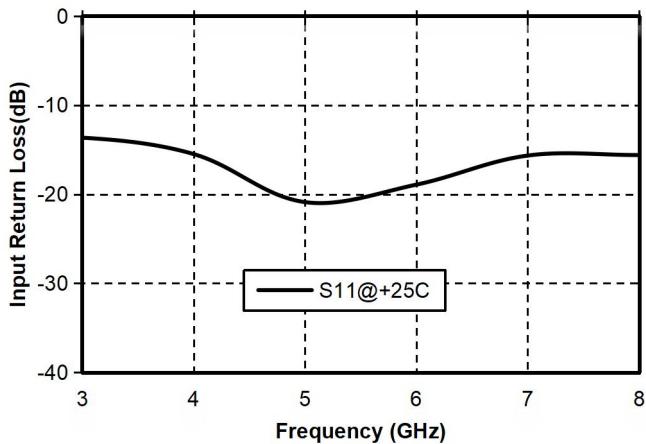
Gain vs. Frequency range



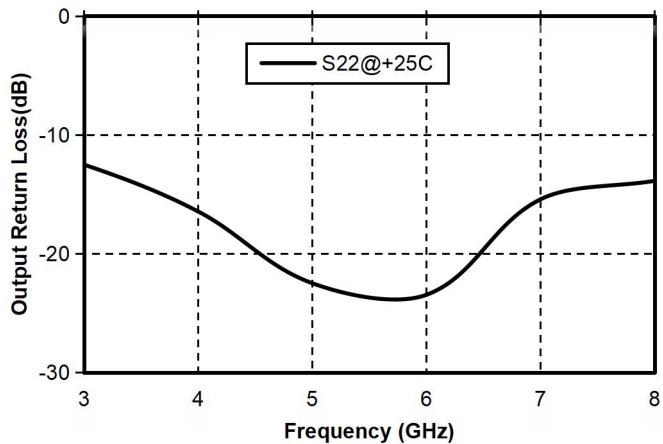
Noise Figure vs. Frequency



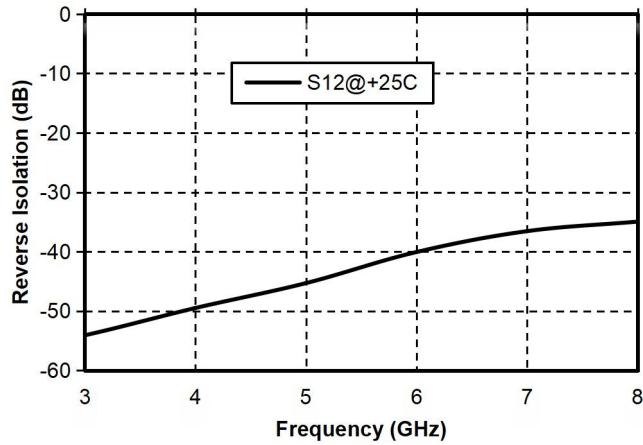
Input return loss vs. Frequency



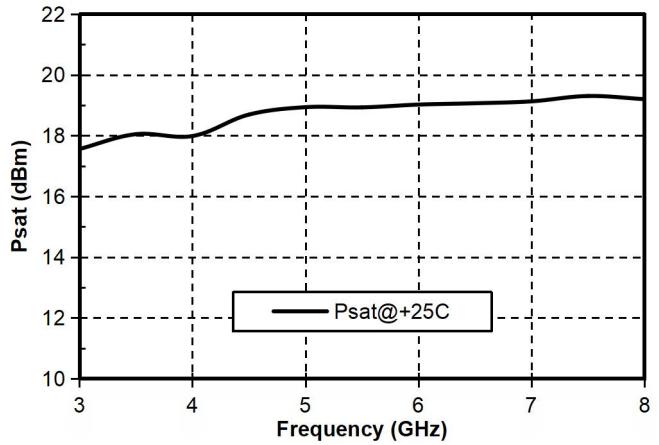
Output return Loss vs. Frequency



Reverse isolation vs. Frequency

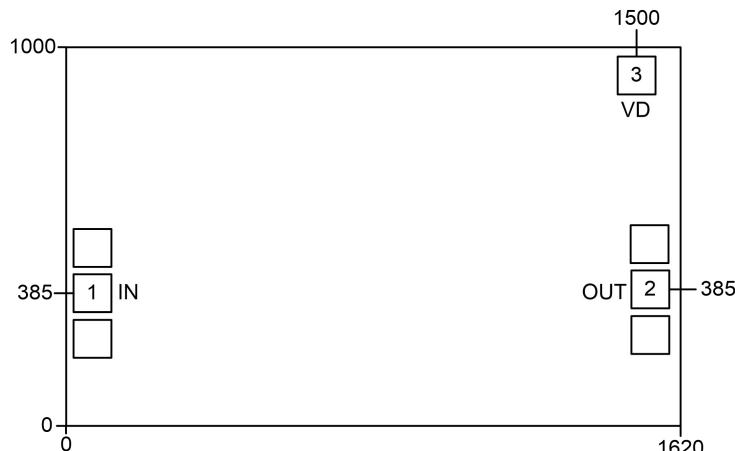


Psat vs. Frequency



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External structure²



【2】The units in the figure are all millimeters, with a tolerance of $\pm 100\mu\text{m}$.

Definition of bonding pressure point

| Bond point number | Functional symbols | Function Description |
|-------------------|--------------------|--|
| 1 | RFIN | RF signal input terminal, no need for DC capacitors. |
| 2 | RFOUT | RF signal output terminal, no need for DC isolation capacitor. |
| 3 | VD | Amplifier drain bias, requires an external 100pF bypass capacitor. |
| Chip bottom | GND | The bottom of the chip needs to be well grounded with RF and DC. |

Recommended assembly diagram

