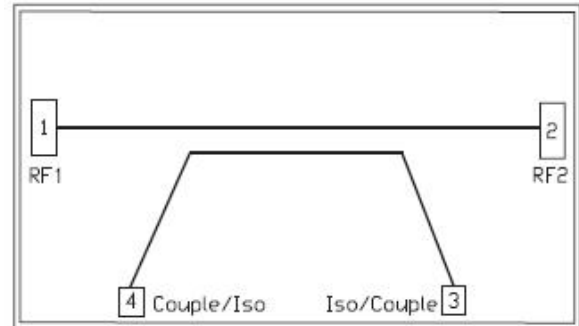


## GaAs MMIC Monolithic Integrated Directional Coupler , 6-18GHz

### Performance characteristics

- Frequency range: 6 -18 GHz
- Insertion loss : 0.15 dB (typ.)
- Coupling: 20dB
- Coupling flatness: 3.0dB
- VSWR: 1.2
- 50Ohm input / output
- 100% on-wafer testing
- Chip size: 2.00 x 0.94 x 0.1mm

### Functional Block Diagram



### Product Introduction

The GDC-061820 single-chip coupler chip covers a frequency range of 6 GHz to 18 GHz with a coupling degree of 20 dB . The chip has an insertion loss of 0.15 dB , a coupling flatness of 3.0 dB , and a port VSWR of 1.2 in the entire operating frequency band. The chip uses an on-chip through-hole metallization process to ensure good grounding, does not require additional grounding measures, and is simple and convenient to use.

#### Use restriction parameter <sup>1</sup>

Maximum input power	+40dBm
Operating temperature	-55 ~ +85°C
Storage temperature	-65 ~ +150°C

【1】 Exceeding any of these maximum limits may cause permanent damage.

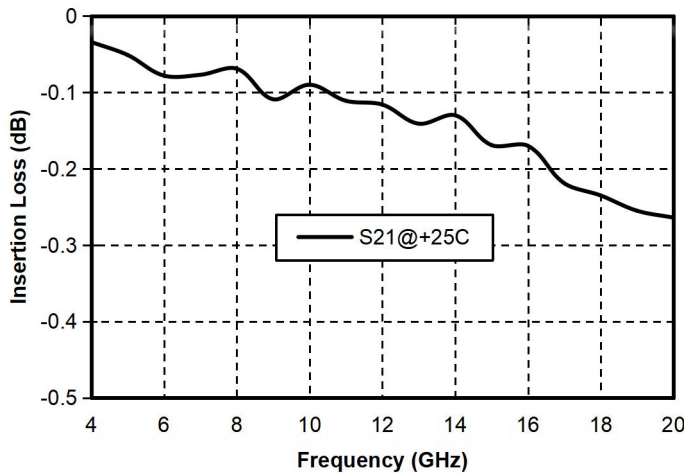
#### Electrical performance parameters ( TA = +25°C)

Index	Minimum	Typical Value	Maximum	Unit
Frequency Range	6-18			GHz
Insertion loss		0.15	0.25	dB
Coupling	18.5	20	21.5	dB
Input return loss	21	23	-	dB
Output return loss	20	23	-	dB
Coupled output return loss	19	19.5	-	dB
Isolation		38		dB

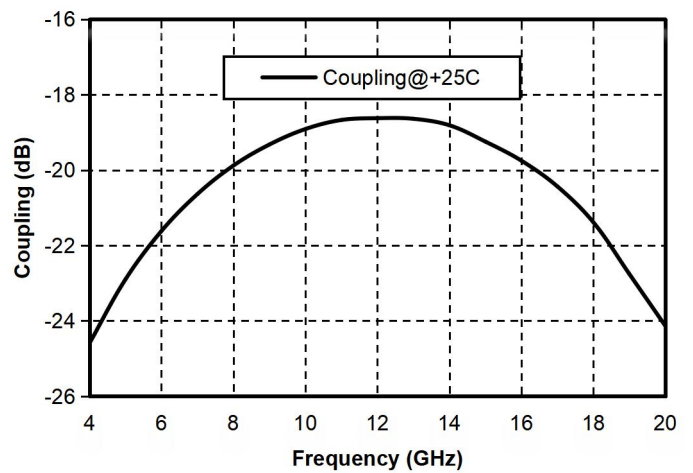
## GaAs MMIC Monolithic Integrated Directional Coupler , 6-18GHz

Main index test curve

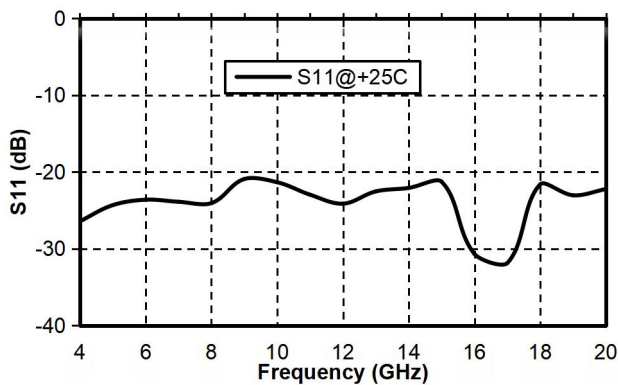
Insertion Loss vs. Operating Frequency



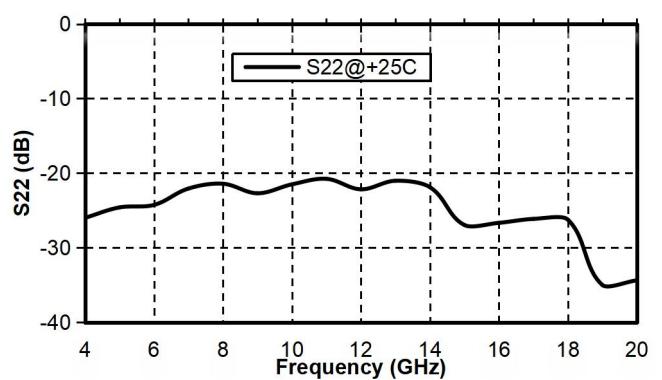
Coupling Degree vs. Operating Frequency



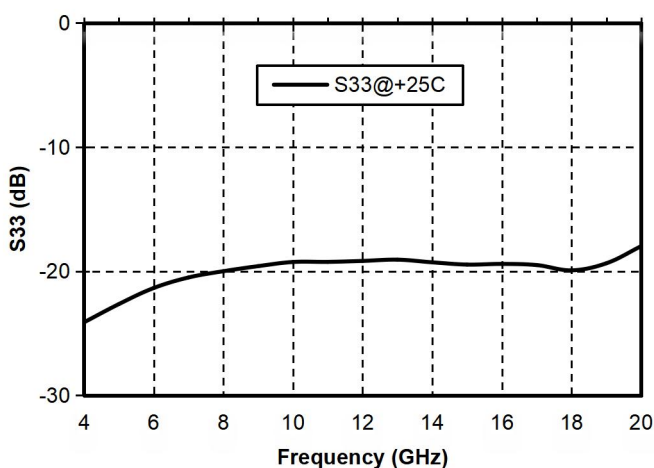
Input Return Loss vs. Operating Frequency



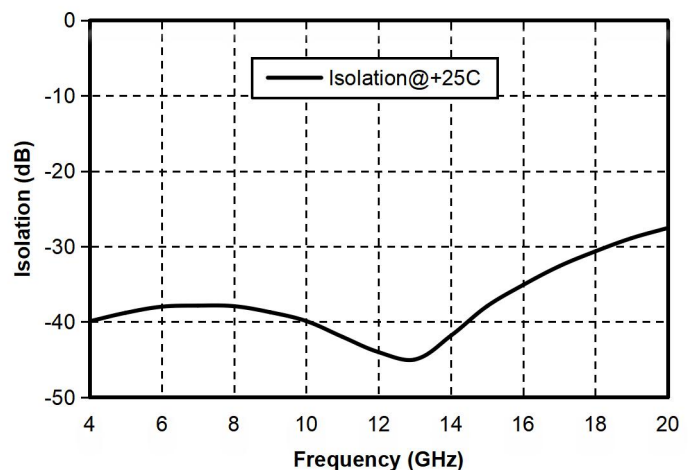
Output Return Loss vs. Operating Frequency



Coupled Output Return Loss vs. Operating Frequency

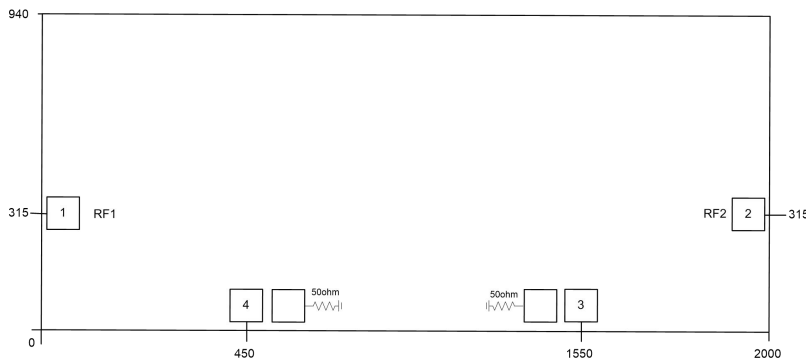


Isolation vs. Operating Frequency



## GaAs MMIC Monolithic Integrated Directional Coupler , 6-18 GHz

Appearance structure <sup>2</sup>



【 2 】 All units in the figure are micrometers

Bonding point definition		
Bonding point number	Function Symbol	Functional Description
1	RF 1	RF signal input /output terminal
2	RF2	RF signal input /output terminal
3	Coupling/Termination	Coupled RF signal output and /or load
4	Coupling/Termination	Coupled RF signal output and /or load
Chip bottom	GND	The bottom of the chip needs to be well grounded to RF and DC

\* At the coupled RF signal output end, users can choose the internal integrated load of the chip or connect an external load.

Recommended assembly drawing

