

GaAs MMIC CNC Attenuator Chip, DC-12GHz

Performance Characteristics

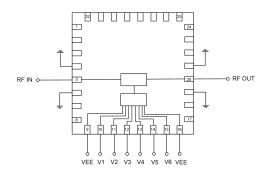
Principle Block Diagram

Frequency range: DC-12GHz Insertion loss: 3.5dB typ Attenuation range: 0.5~31.5dB

Bit count: 6

Attenuation accuracy (RMS): 0.4dB Additional phase shift (RMS): 1.2°

Standing wave: 1.3:1 50Ohm input/output Chip size: QFN 5X5



Product Introduction

GDA-0012-6E-PD-CQ5 is a GaAs MMIC 6-bit CNC attenuator chip, with a frequency range of DC~12GHz, insertion loss of 3.5dB, switching speed of 50ns, integrated driver inside the chip, powered by -5V and controlled by 0/+5V. The amplifier adopts a 5X5mm surface mount lead-free ceramic tube shell, which can achieve airtight packaging. The surface of the pin pads is treated with gold plating technology, suitable for reflow soldering installation process.

Use restriction parameters¹				
Power supply voltage range	-6V			
Control voltage range	-0.5V~+5.5V			
Maximum input power	+27dBm			
Working temperature	-55 ~ +85°C			
Storage temperature	-65 ~ +150°C			

[1] Exceeding any of the above maximum limits may result in permanent damage.

Electrical parameters (Ta=+25° C, VEE=-5V, 0/+5V control)						
Index	minimum value	Typical value	Maximum value	Unit		
Frequency range		GHz				
Insertion loss	-	-	dB			
Attenuation range		dB				
Attenuation step		dB				
Attenuation number		bite				
Attenuation accuracy (all frequency bands)	-	-1.5 ~ +1.0	-	dB		
Attenuation accuracy RMS	-	0.4	-	dB		
Additional phase shift RMS	-	1.2		degree		

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Input return loss	-	19	-	dB
Output Return Loss	-	17	-	dB
Switching speed	-	50	-	ns
P-1dB	-	22	-	dBm
Current	-	10	-	mA

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Main indicator testing curve

0

3

Insertion loss vs. frequency

O

S21

-2

-3

-4

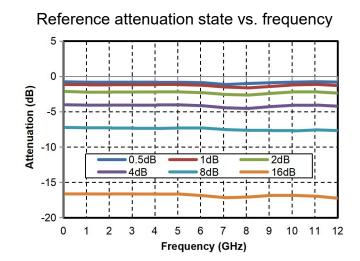
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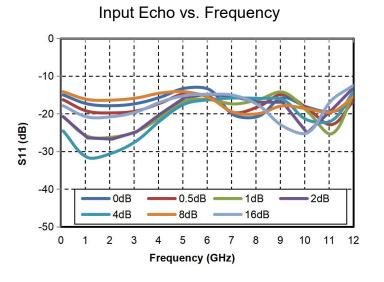
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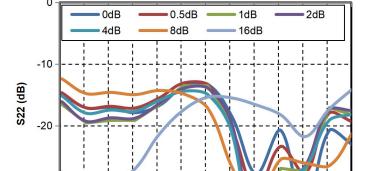
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Frequency (GHz)

10







Output Echo vs. Frequency

Attenuation accuracy vs. frequency

Attenuation accuracy RMS vs. frequency

5 6

Frequency (GHz)

8 9 10 11

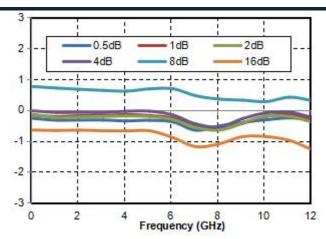
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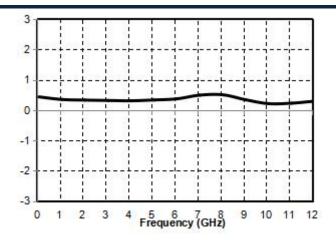
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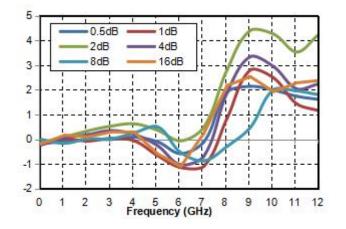




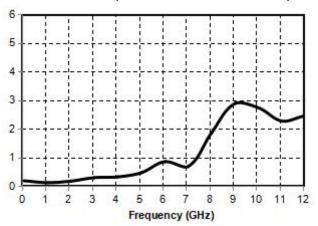


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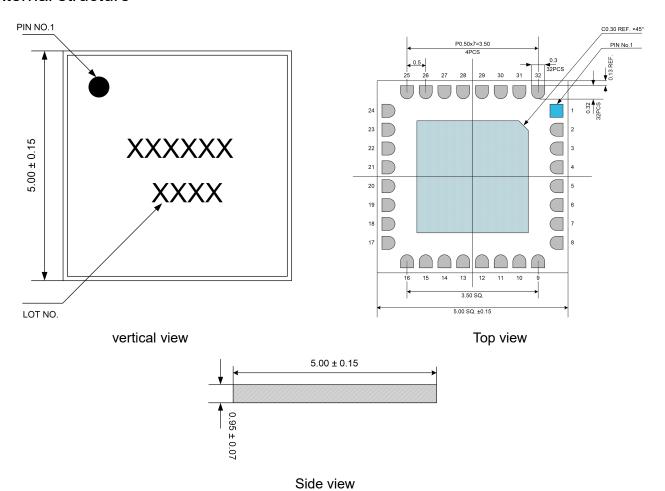
Additional phase shift vs. frequency



Additional phase shift RMS vs. frequency



External structure



The units in the figure are all millimeters, with an unspecified tolerance of \pm 0.15mm

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Truth table

V1	V2	V3	V4	V5	V6	VEE	Conduction pathway	
0	0 0 0 0	0		0	0		5) (Initial state N=0: attenuation
U		-5V	amount is 0					
±5\/	+5V 0 0	0	0	0	0	-5V	Attenuation state N=1:	
+3ν		0	U	U	-3V	Attenuation amount is 0.5		
0	. <i>E</i> \/	0		-5V	Attenuation state N=2:			
0	+5V 0 0 0 0	-5v	Attenuation amount is 1					
0	0 +5V 0 0 0	0	-5V	Attenuation state N=4:				
		+30	U	U	U	-50	Attenuation amount is 2	
0	0	0	+5V	0	0	-5V	Attenuation state N=8:	

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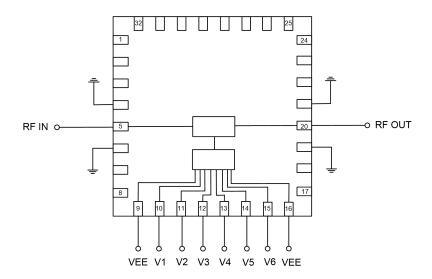
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							Attenuation amount is 4
0	0 0 0 0 +5V 0		Attenuation state N	Attenuation state N=16:			
U		0 +50	U	-5V	Attenuation amount is 8		
0	0	0	0	0	. <i>E</i> \ /	5 \/	Attenuation state N=32:
0	0 0 0	U	0	+5V	-5V	Attenuation amount is 16	

Pin Definition				
Pin number	Functional symbols	Function Description		
5	IN	The signal input terminal is externally connected to a 50 ohm circuit, and there is no integrated DC isolation capacitor inside the chip		
20	OUT	The signal output terminal is externally connected to a 50 ohm circuit, and there is no integrated DC isolation capacitor inside the chip		
9、16	VEE	The chip power port can be connected to the VEE on one side		
10、11、12、13、14、15	VC	Attenuation control pads, refer to the truth table for attenuation control		
4、6、19、21	GND	The pins should have sufficient and good contact with the RF and DC ground		
Chip bottom	GND	The bottom of the chip needs to have sufficient and good contact with RF and DC ground		
other	NC	Pin suspended, can be grounded		

Recommended circuit



Connect the VEE on either side.

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